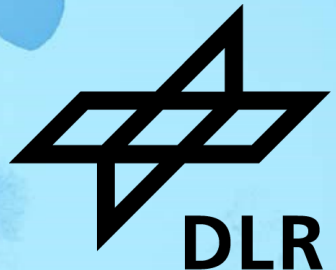


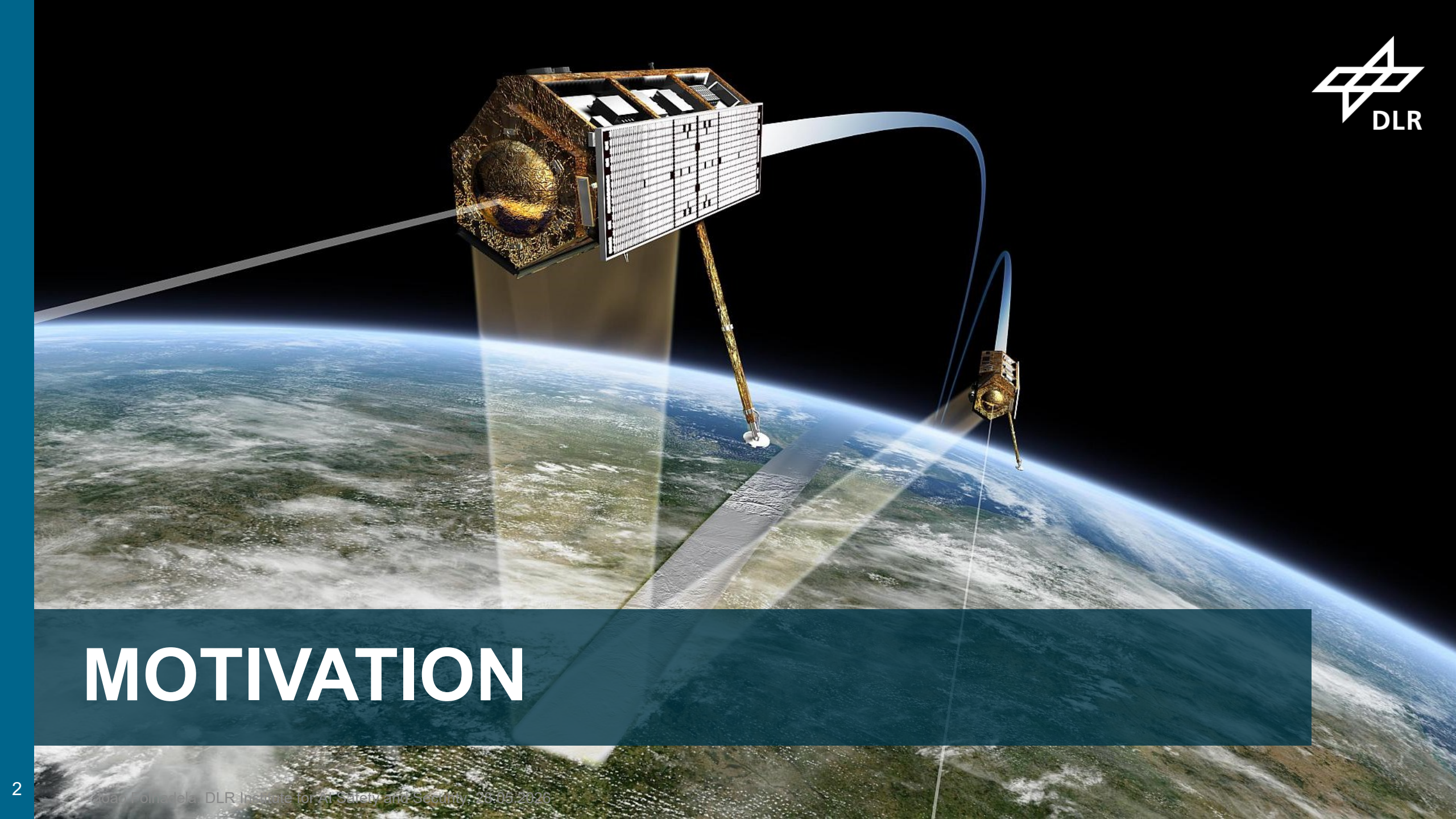
NGRC ON SOC FOR ONLINE LEARNING OF DYNAMICAL SYSTEMS

João C. L. Folhadela & Hans-Martin Rieser

German Aerospace Center (DLR) – Institute for AI Safety and Security

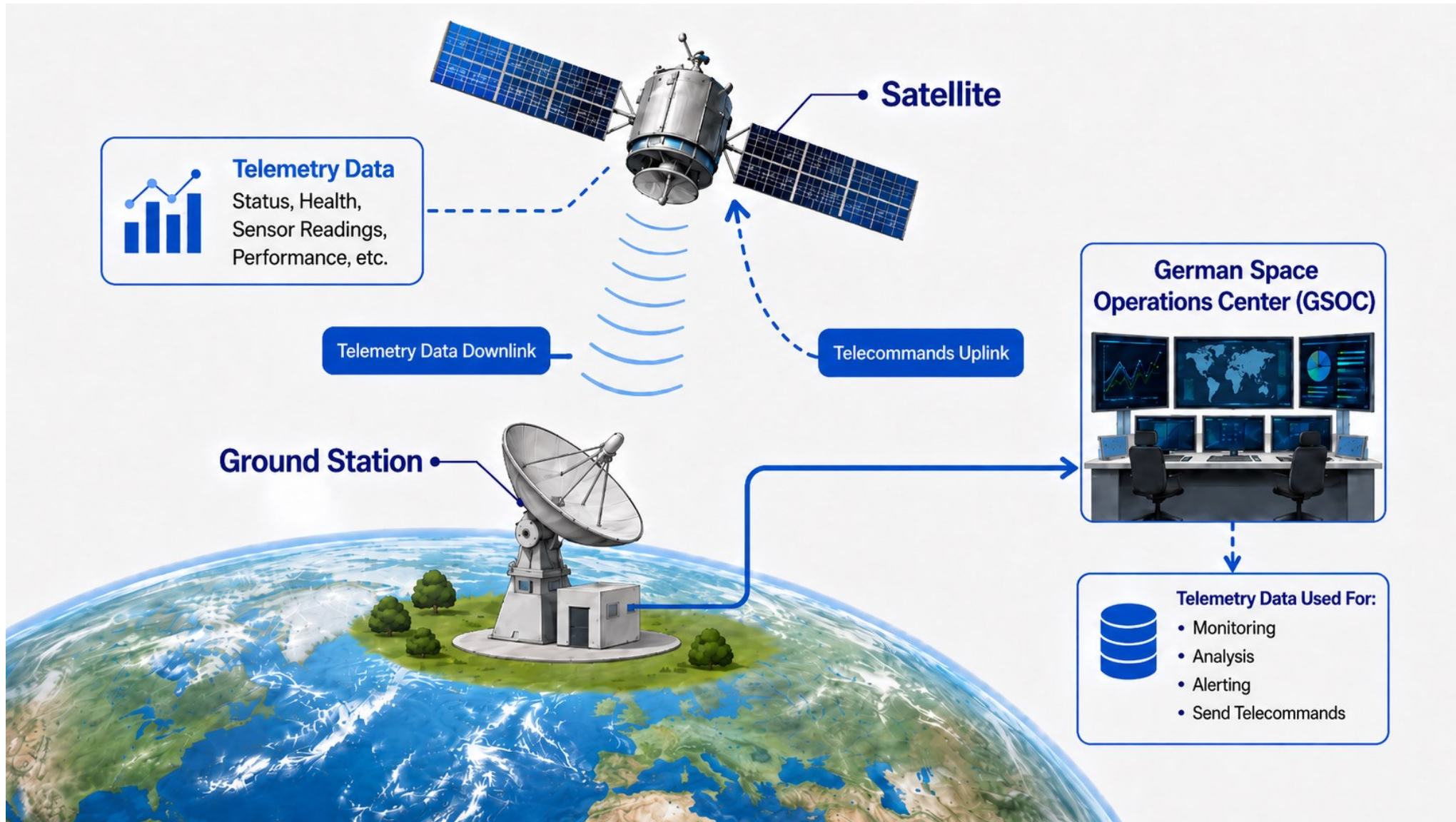
3rd FPGA Developer's Forum (FDF) meeting 2026, CERN



A detailed illustration of an Orbital Transfer Vehicle (OTV) in space. The OTV is a large, boxy spacecraft with a gold-colored thermal blanket on one side and a white grid-like structure on the other. It is shown in the process of transferring a smaller satellite from one orbit to another. A long, thin cable or arm extends from the OTV towards the smaller satellite. The background shows the Earth's horizon with a blue atmosphere and a green and brown landmass. The word "MOTIVATION" is written in large, white, bold, sans-serif capital letters across the bottom of the image.

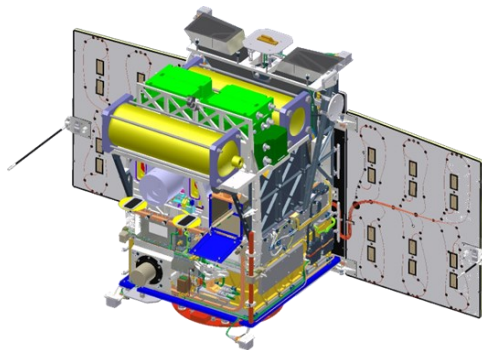
MOTIVATION

Motivation – Satellite Telemetry Data & Telecommands

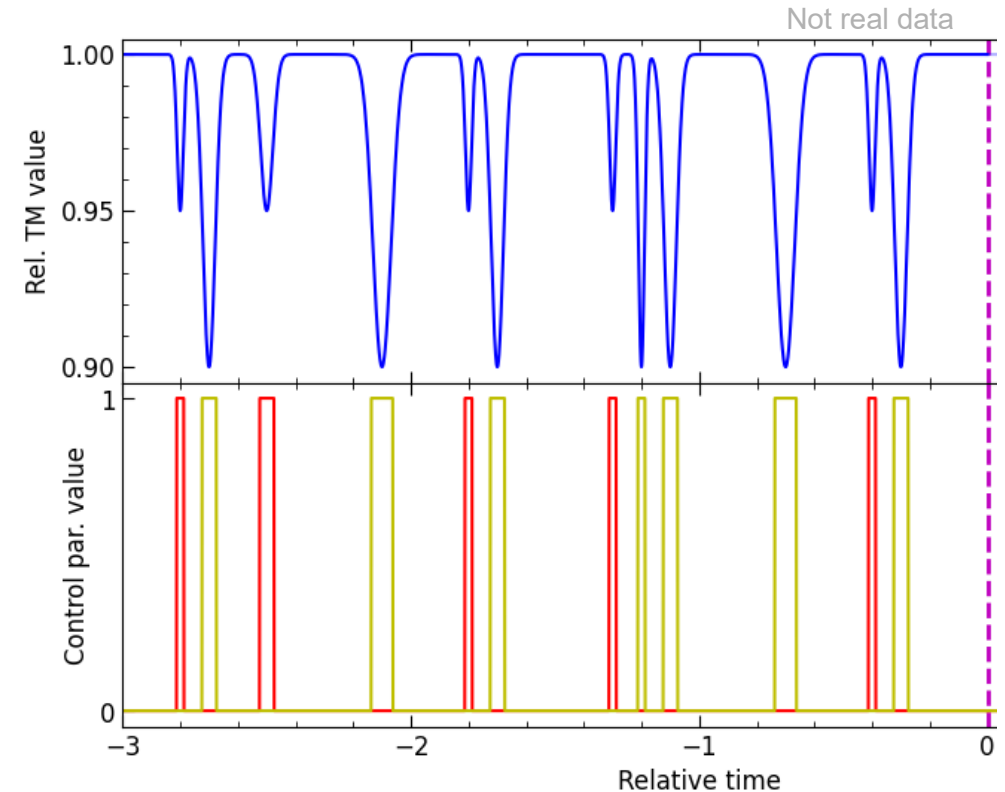


Motivation – Battery Voltage vs Telecommands

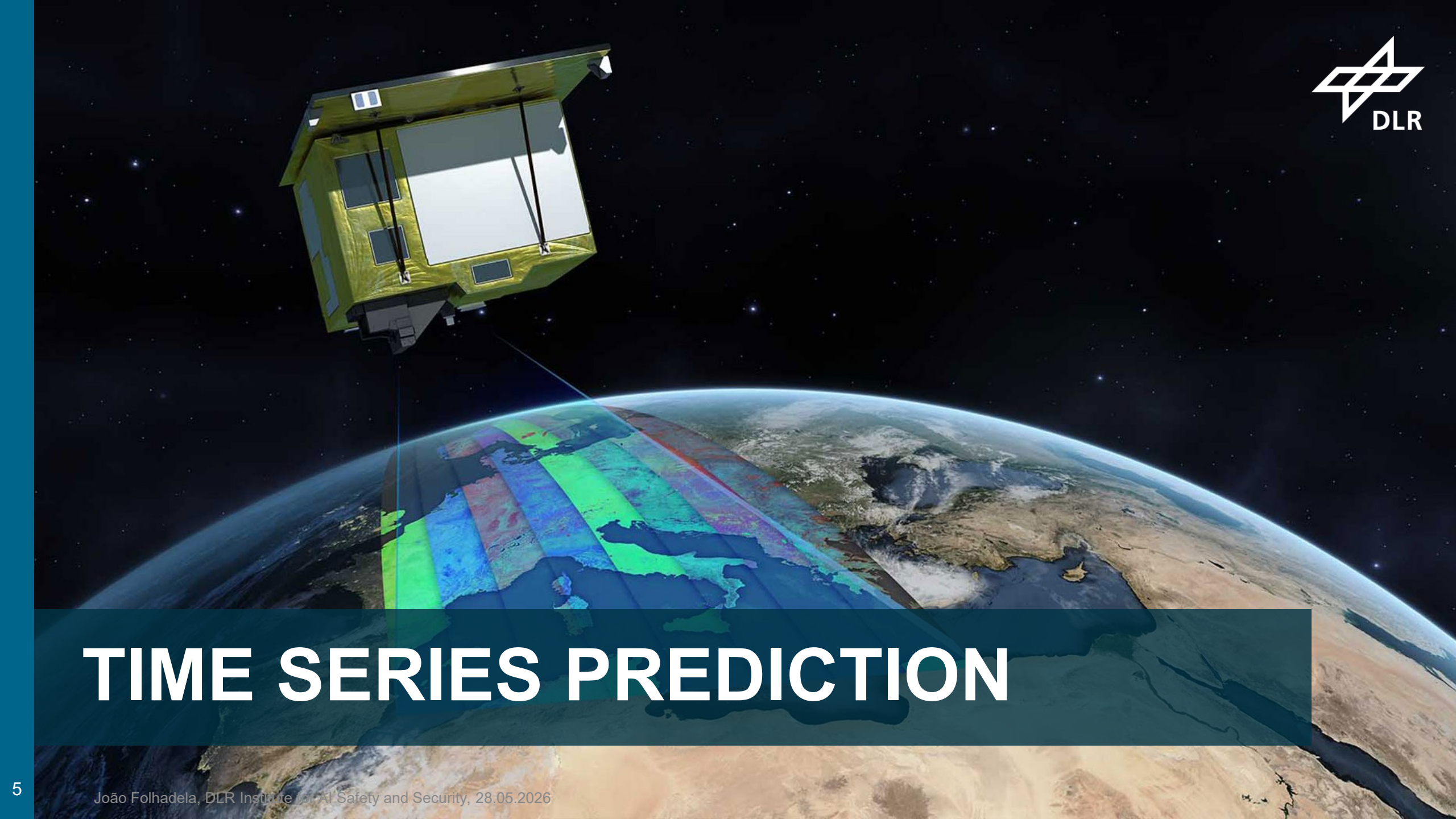
- Too low voltage imperils satellite operations
- Impacts:
 - Use of payload instruments
 - Eclipses
 - Orientation of solar panels
 - Temperature and ageing of battery



BIROS Image: DLR, CC BY-NC-ND 3.0



On-board prediction of the voltage due to planned Telecommands so impacts can be minimized.

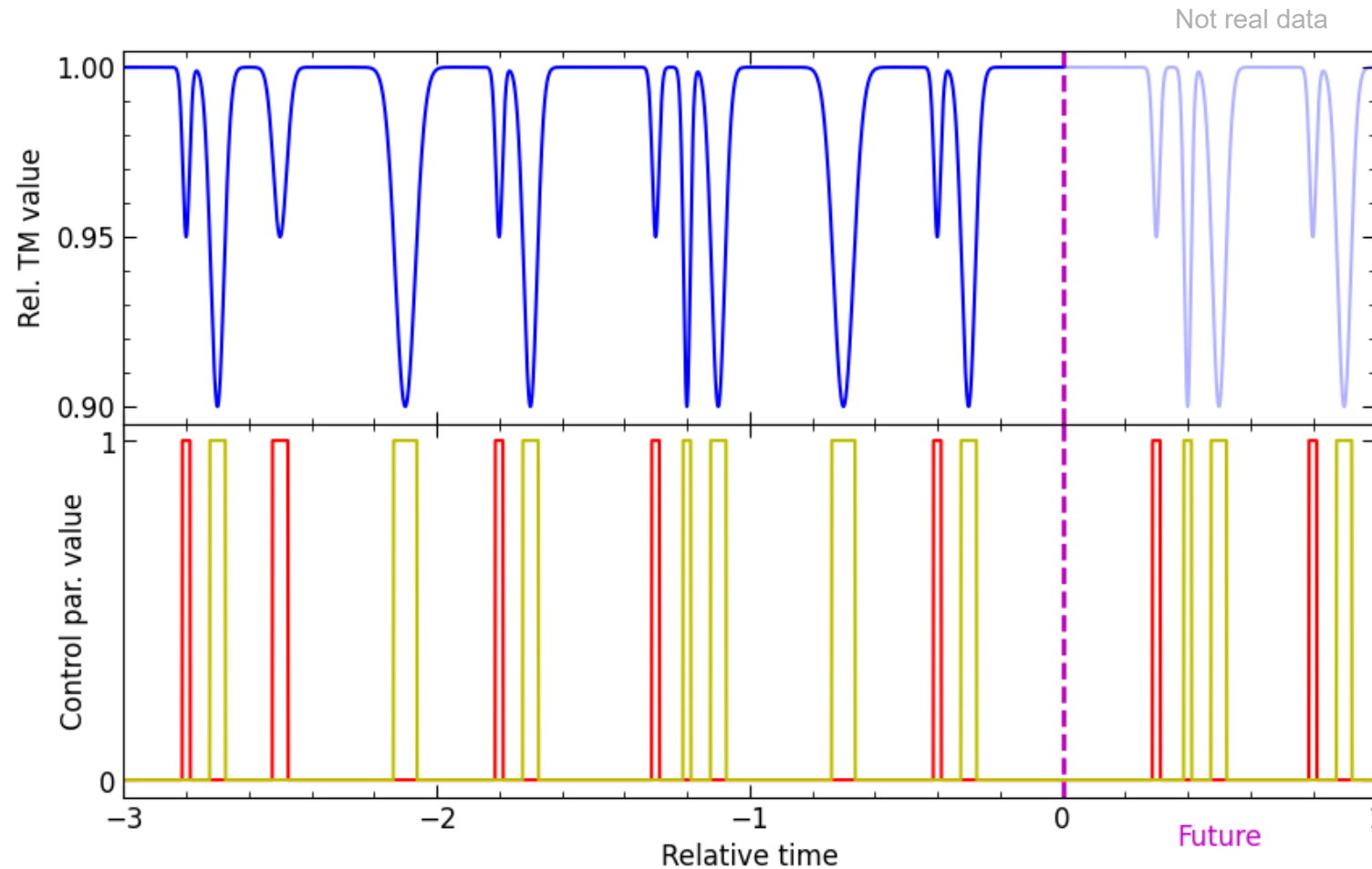
A satellite with a gold-colored body and a large white rectangular panel is shown in orbit above the Earth. The satellite is emitting a red laser beam that points down at a specific location on the Earth's surface. This location is highlighted with a colorful, multi-colored rectangular area, possibly representing a data footprint or a specific region of interest. The Earth's surface shows landmasses and oceans, with a blue atmospheric layer visible at the horizon. The background is the blackness of space with some distant stars.

TIME SERIES PREDICTION

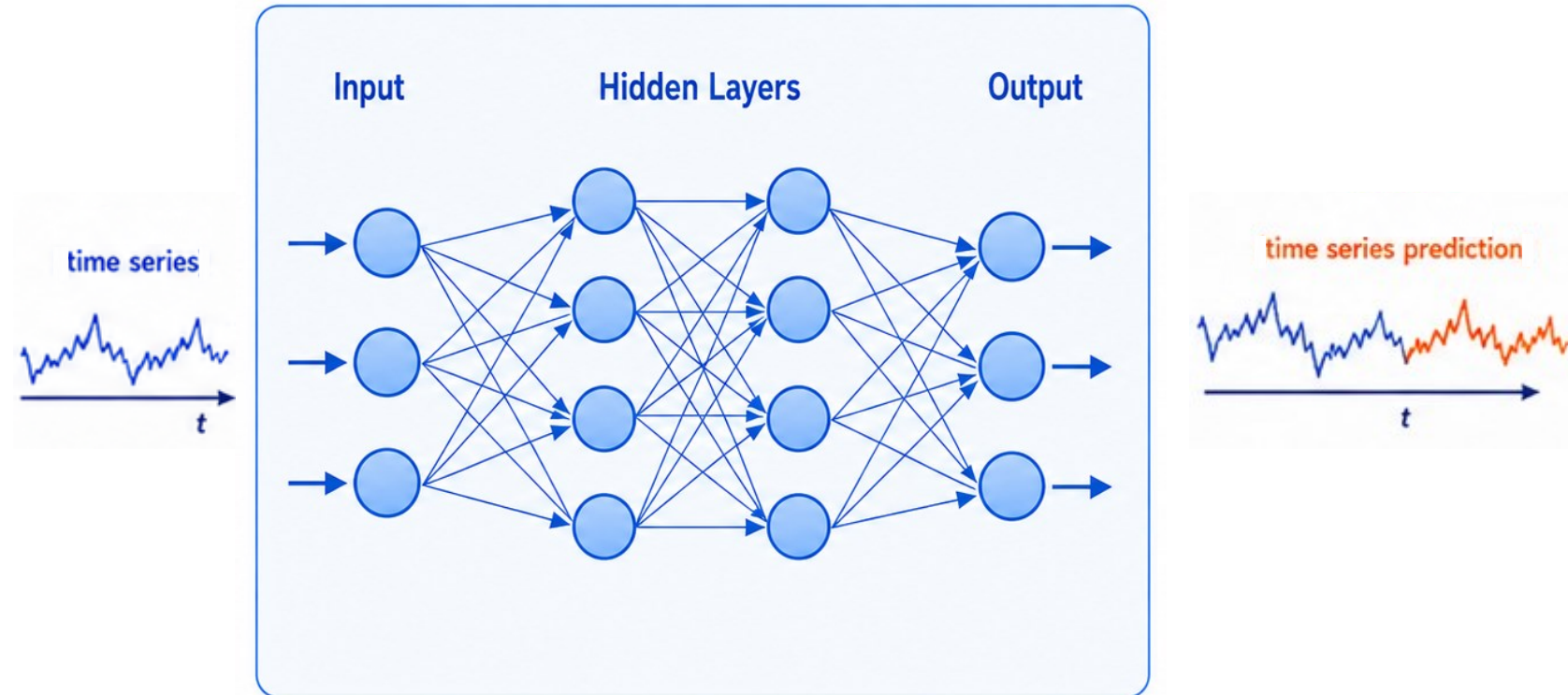
Time Series Prediction – Satellite battery voltage

Received
telemetry

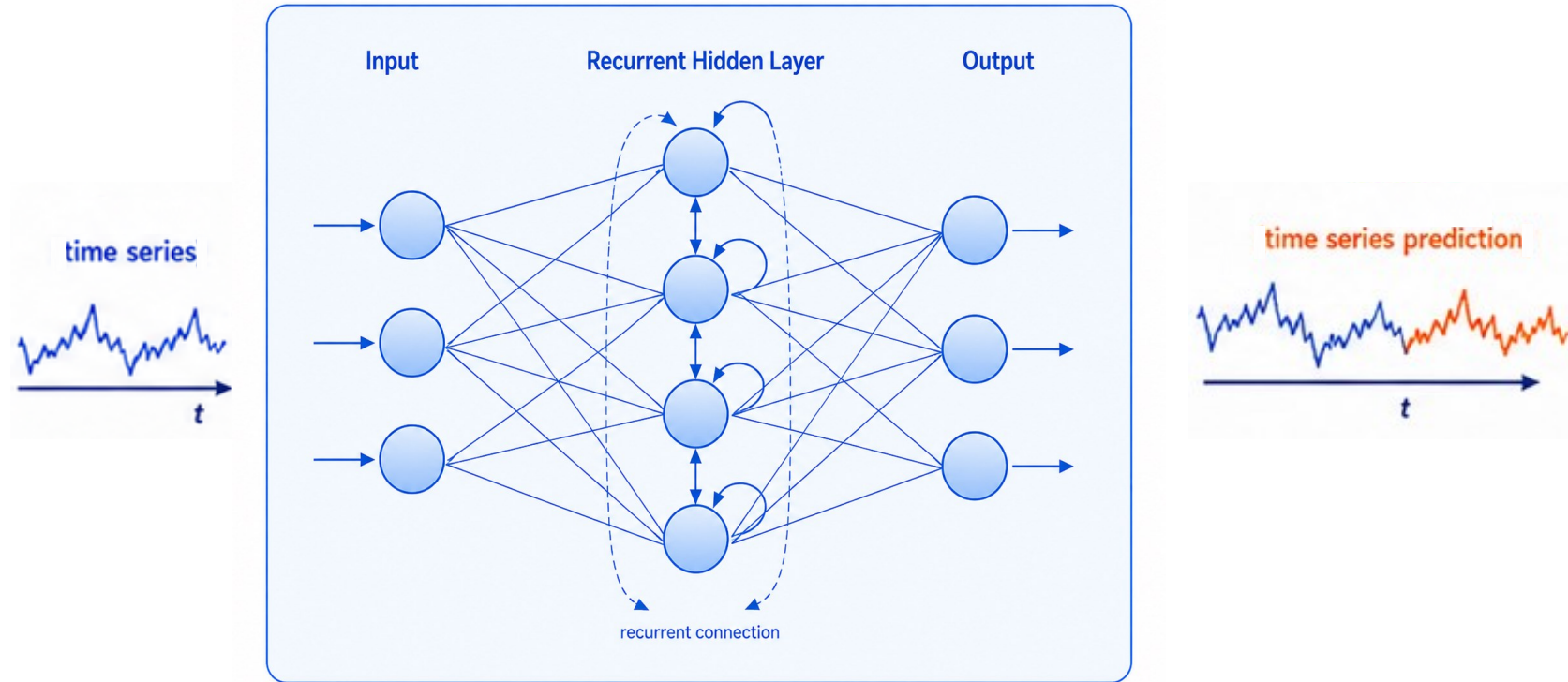
Control par.
based on
executed and
planned TC



Neural Network

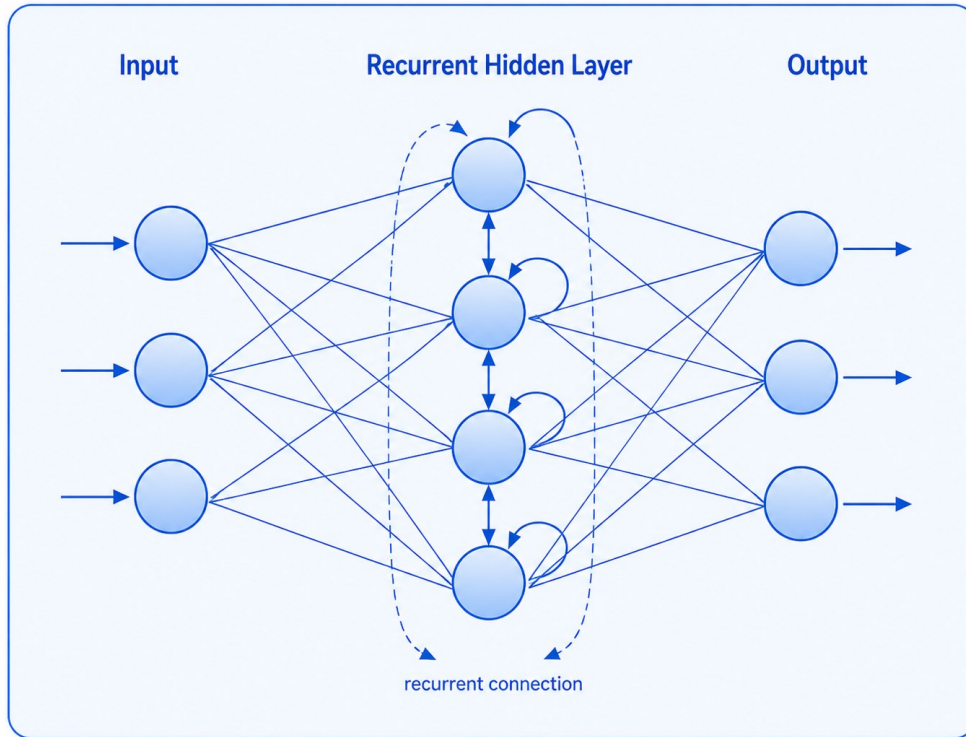


Recurrent Neural Network



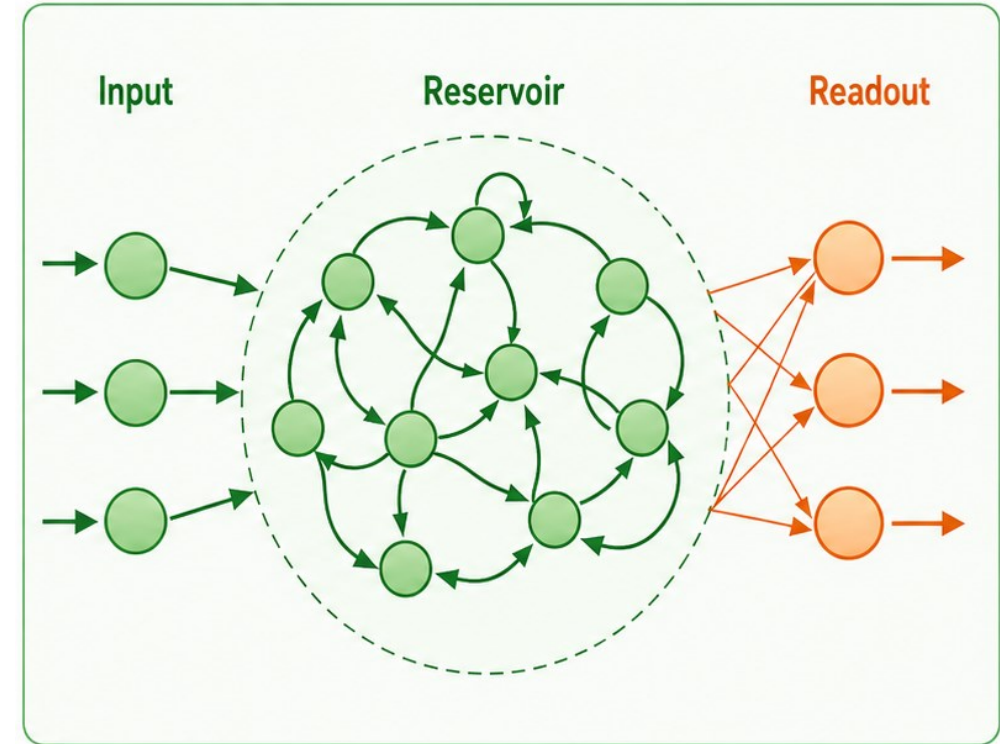
- + Increases accuracy
- Expensive train

Recurrent Neural Network



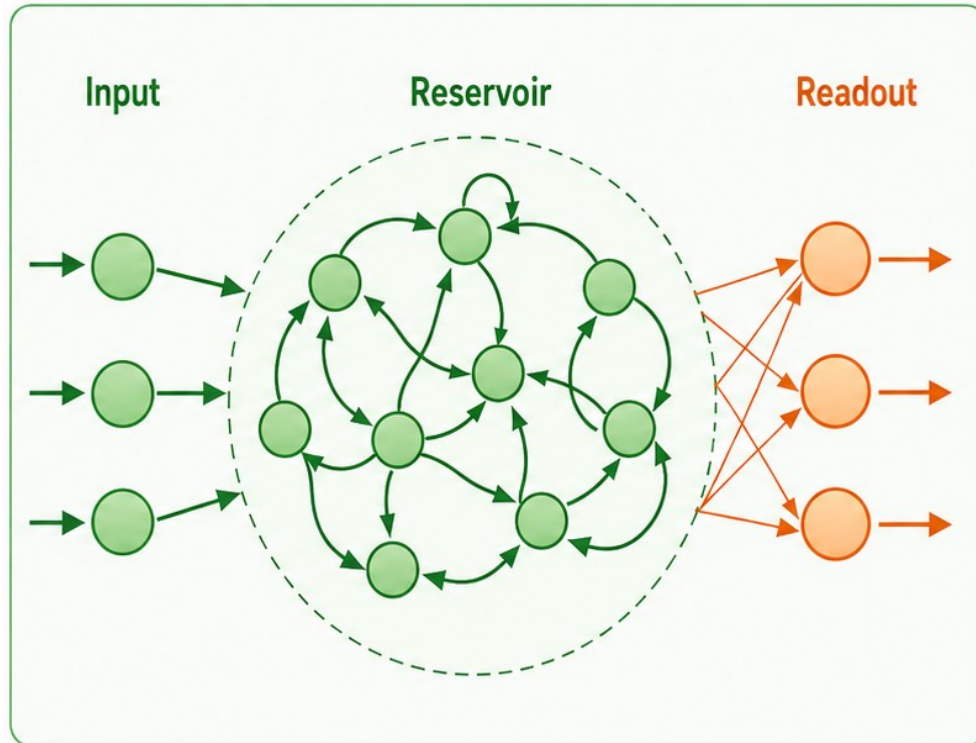
All weights (connections) are trained

Reservoir Computing



Only readout weights are trained

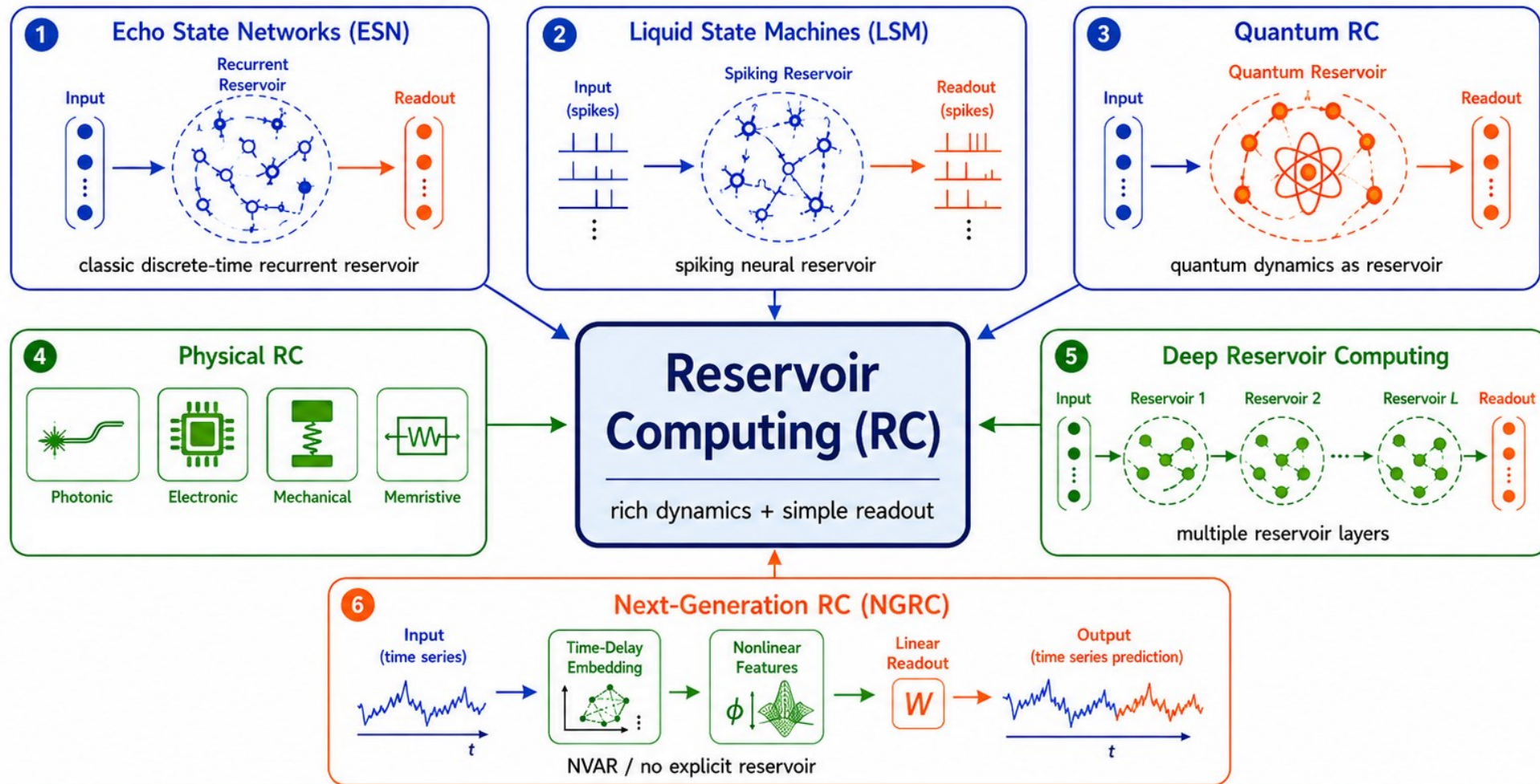
Reservoir Computing



Better fit for FPGA on-board deployment compared to classic neural networks:

- Simpler training/readout stage, often a regression
- Custom datapaths
- Simpler nonlinear operations

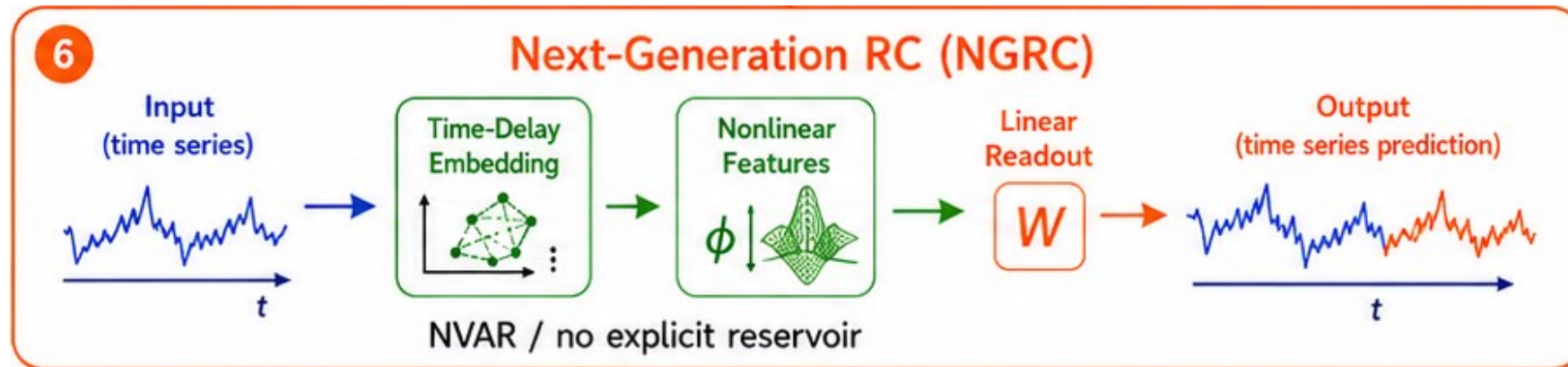
Introduction



All variants keep the same principle: complex dynamics transform the input, while only a simple readout is trained.

Next Generation Reservoir Computing

- NGRC-RC equivalence: “There exists a Nonlinear Vector Autoregression (NVAR) that can perform equally well as an optimized RC” – D. Gauthier et. al Nature Commun 2021
- Faster training and interpretable results.
- FPGA fit – no activation functions, fewer parameters, trivial implementation

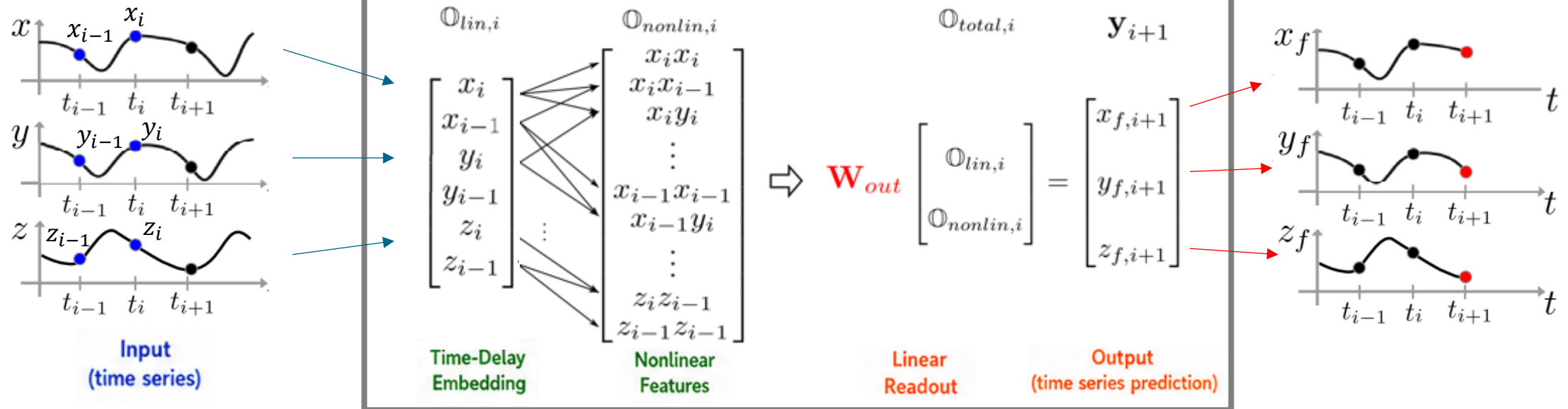
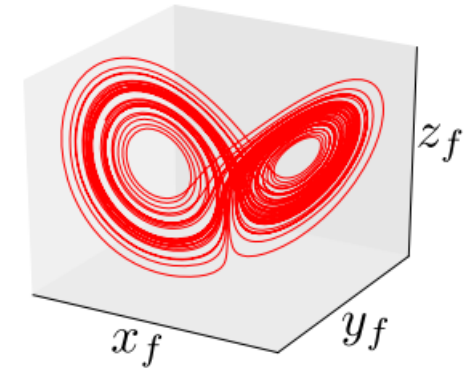
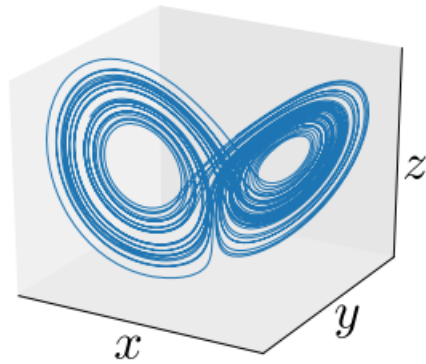


Proof of Concept – Predicting the Lorenz Attractor

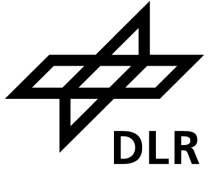
Ridge Regression Training

$$\mathbf{W}_{out} = (\mathbf{r}^T \mathbf{r} + \beta \mathbf{I})^{-1} \mathbf{r}^T \mathbf{Y}_{train}$$

$$\mathbf{r}^T = [\mathbb{O}_{total,0}^T \quad \dots \quad \mathbb{O}_{total,i}^T], \quad \mathbf{Y}_{train} = [y_0 \quad \dots \quad y_i]$$



2nd FDF 2025 – NRGC Accelerator

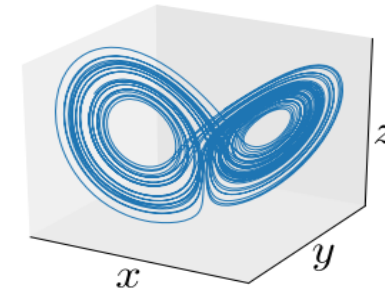


Software

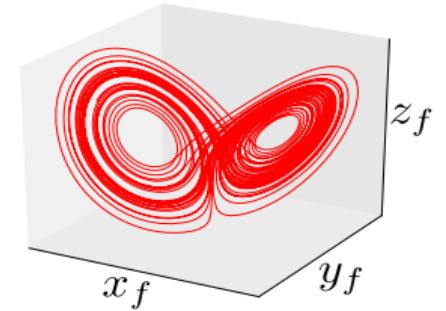
- Model with only 81 parameters
- Trained with 400 timesteps

FPGA accelerator on Zynq7000 SoC

- 2.5 μ s/inference @125MHz with 32-bit float precision
- On-chip estimated power 300 mW
- Fixed weights, UART communication
- Accuracy...

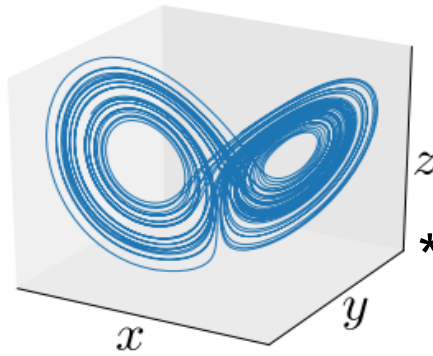


Real



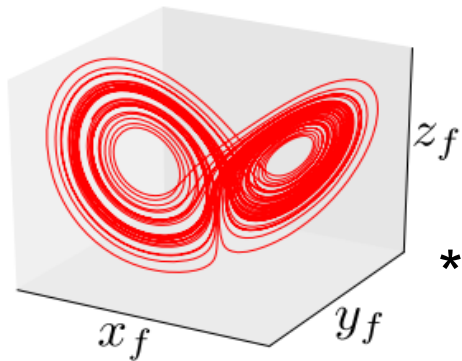
Predicted

Real vs. Predicted Lorenz



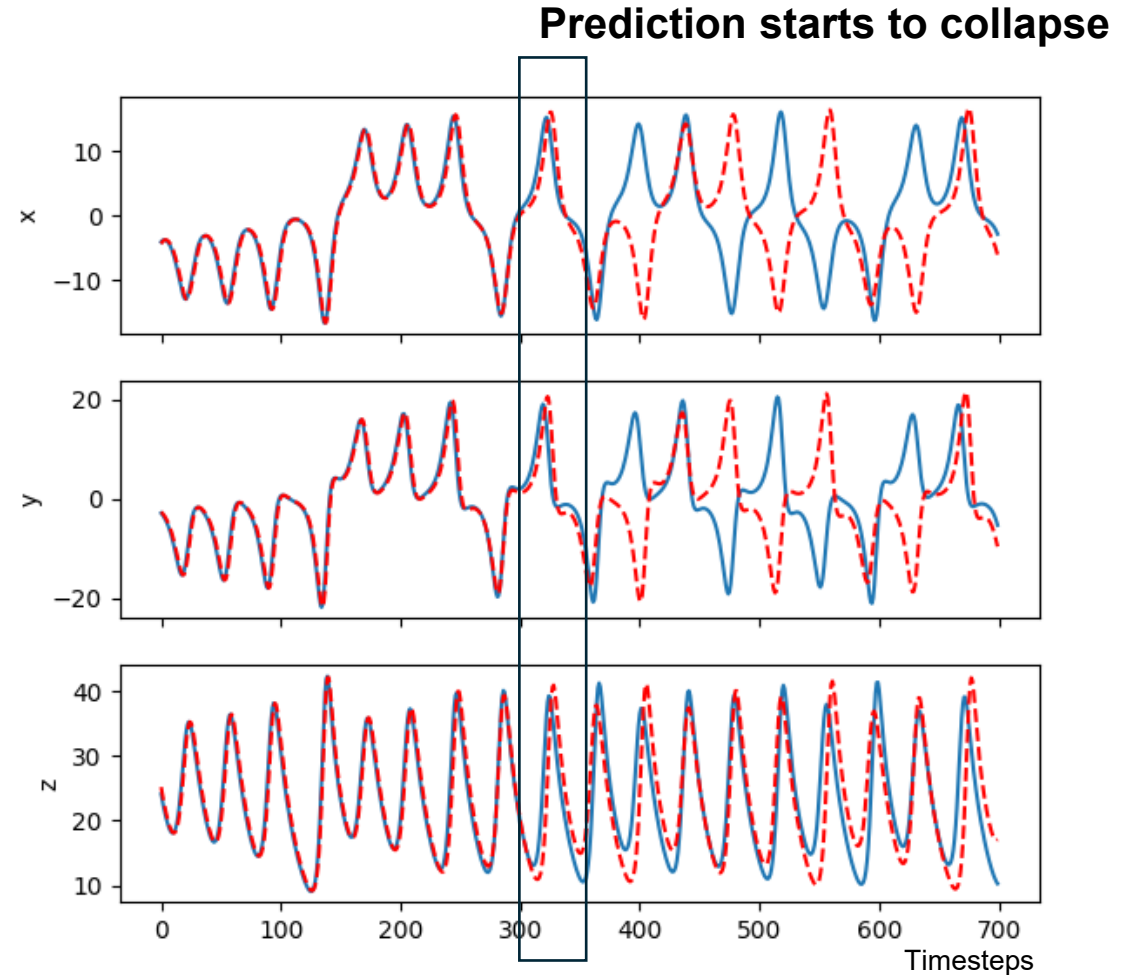
Real

$$*\lambda_{max} = 0.79$$



Predicted

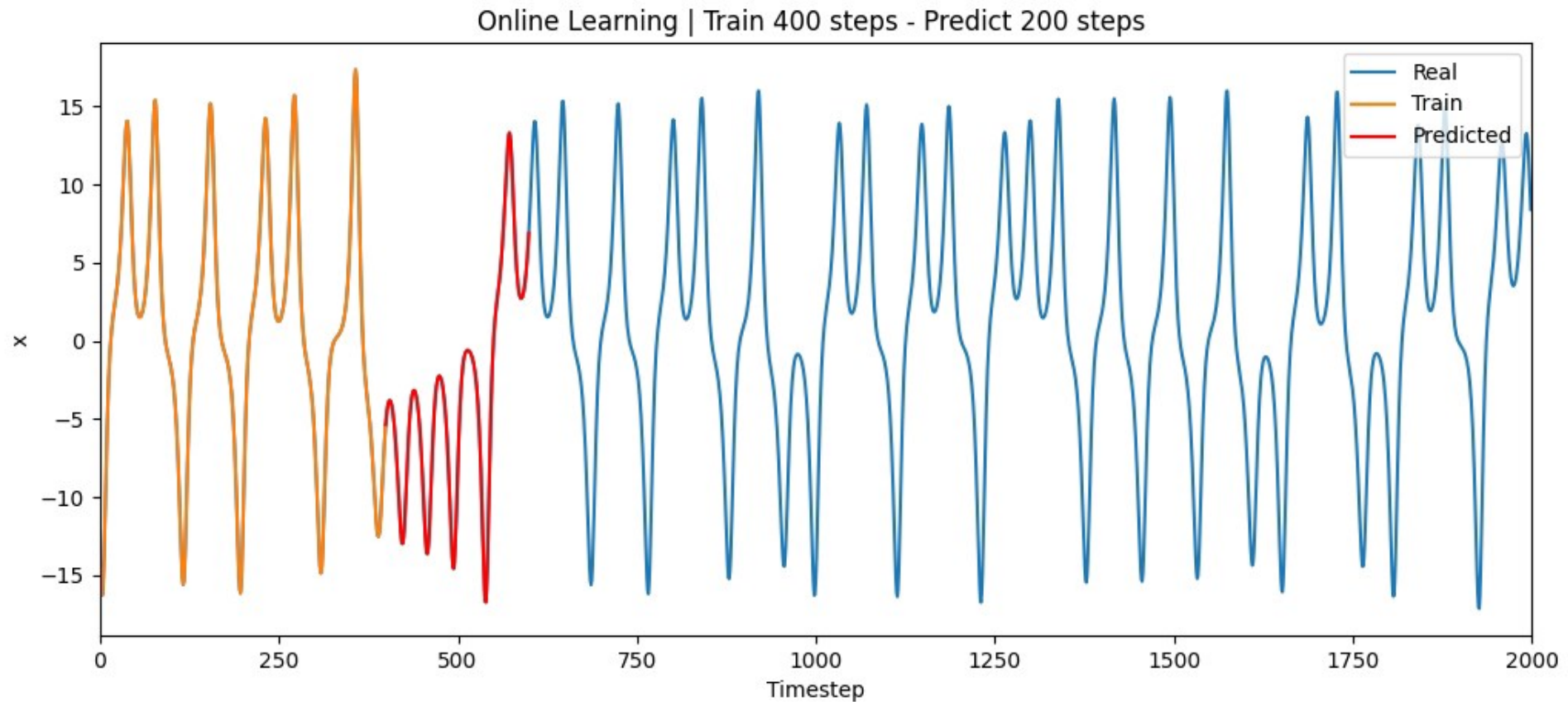
$$*\lambda_{max} = 0.77$$



***Largest Lyapunov Exponent λ_{max}** : measures sensitivity to initial conditions. Similar values suggest similar chaotic behaviour.

Online Learning

Since NGRC train is cheap, continuously retrain on new data





ONLINE LEARNING ON SOC

- Ridge regression training on FPGA fabric is non-trivial:

Lots of MACs

Memory

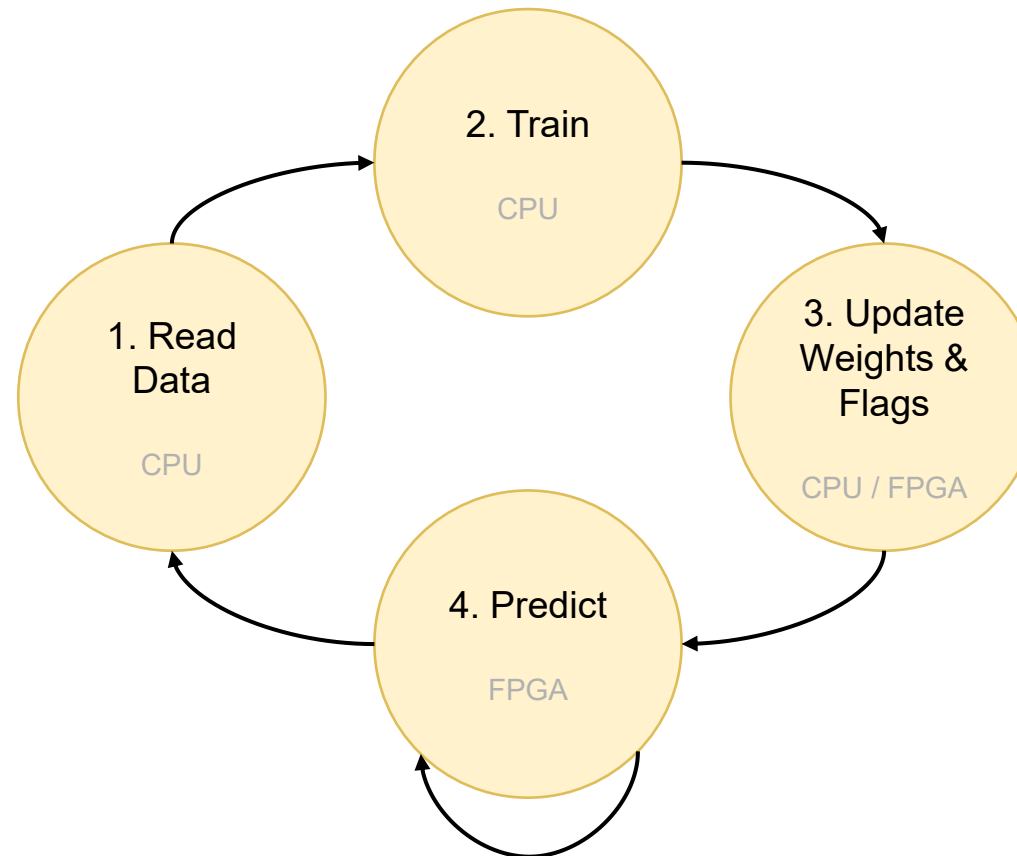
$$\mathbf{W}_{out} = (\mathbf{r}^\top \mathbf{r} + \beta \mathbf{I})^{-1} \mathbf{r}^\top \mathbf{Y}_{train}$$

Matrix Inversion

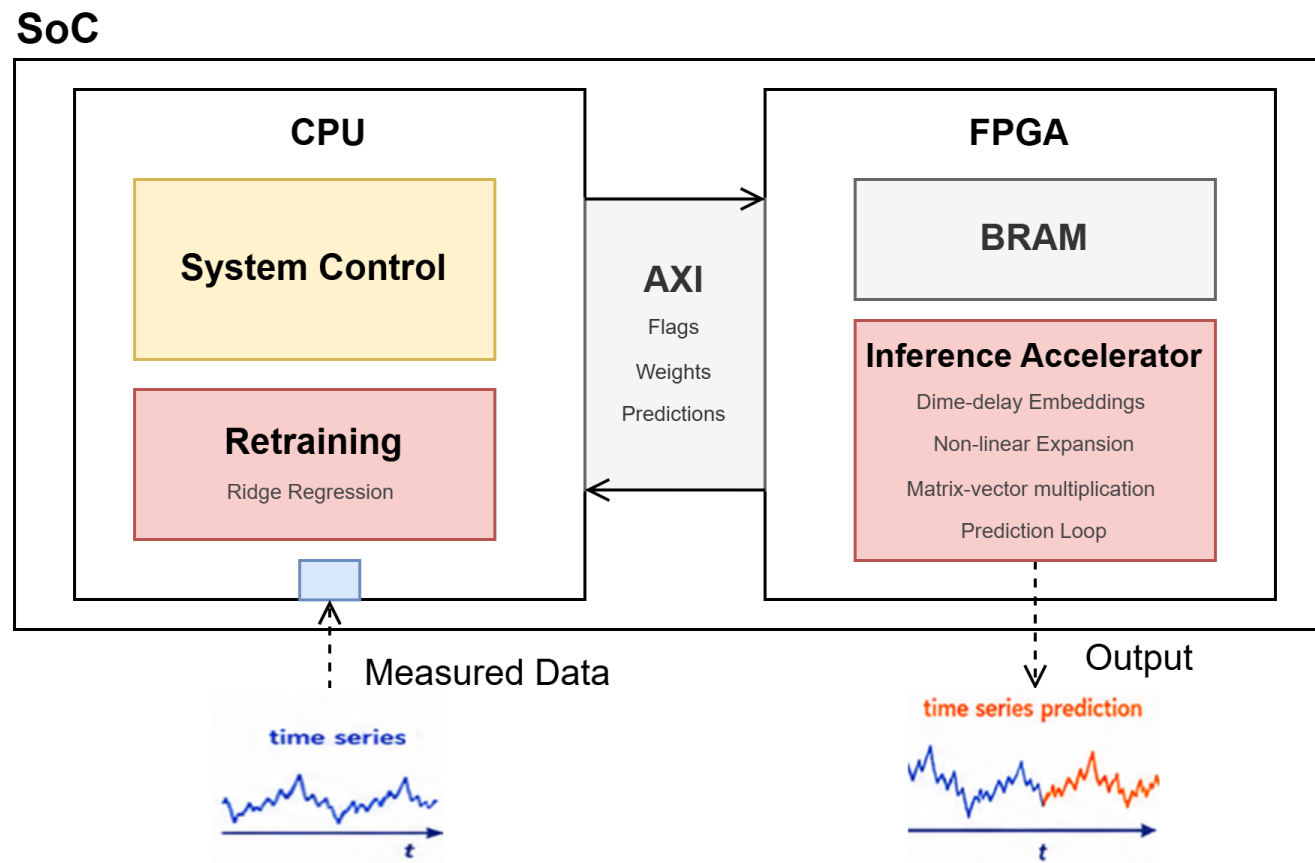
→ Solution: use Zynq's Processing System (PS) CPU for training...

Online Learning on SoC

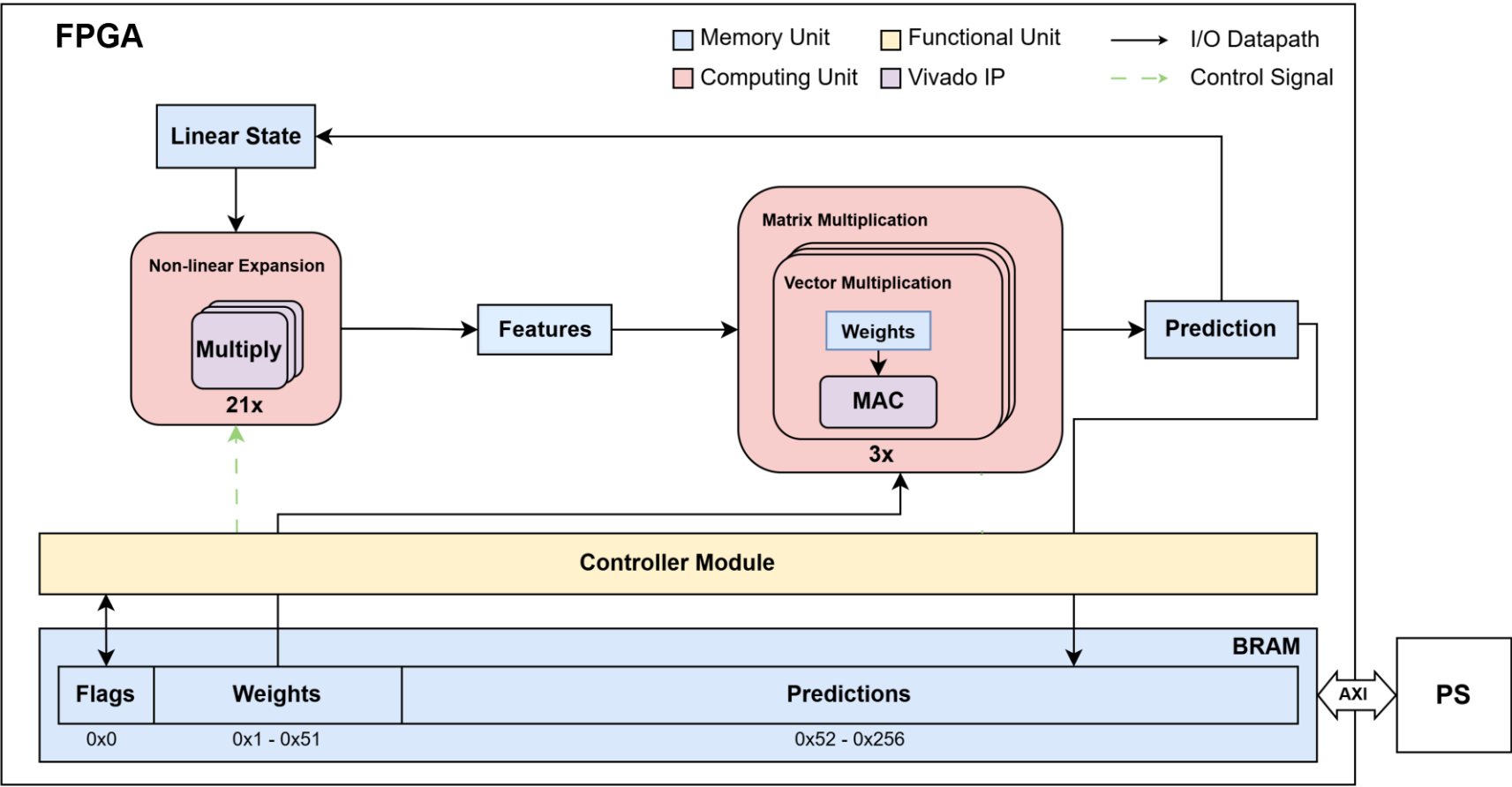
- Solution: use Zynq's Processing System (PS) CPU for training...
- And Programmable Logic (FPGA) to accelerate inference.



System Architecture



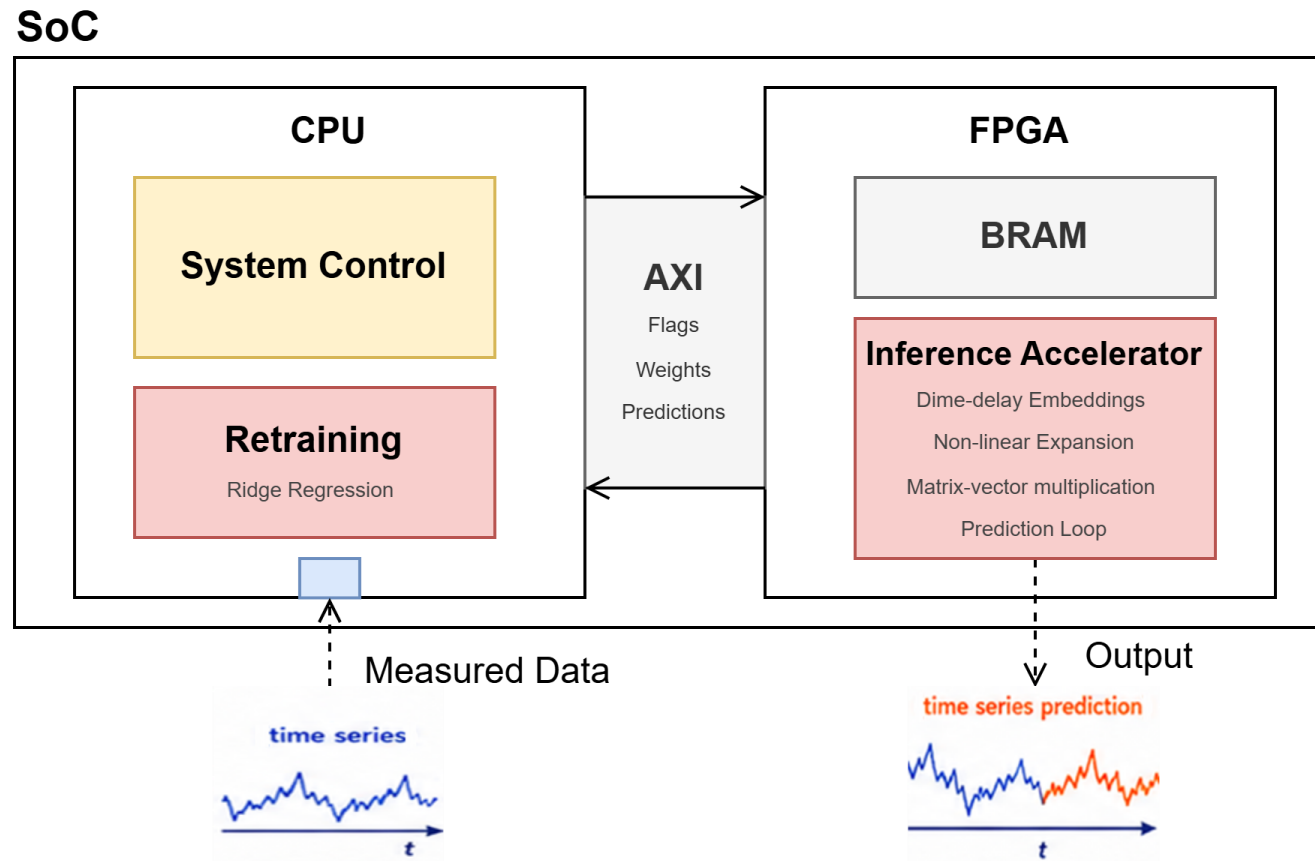
System Architecture - FPGA



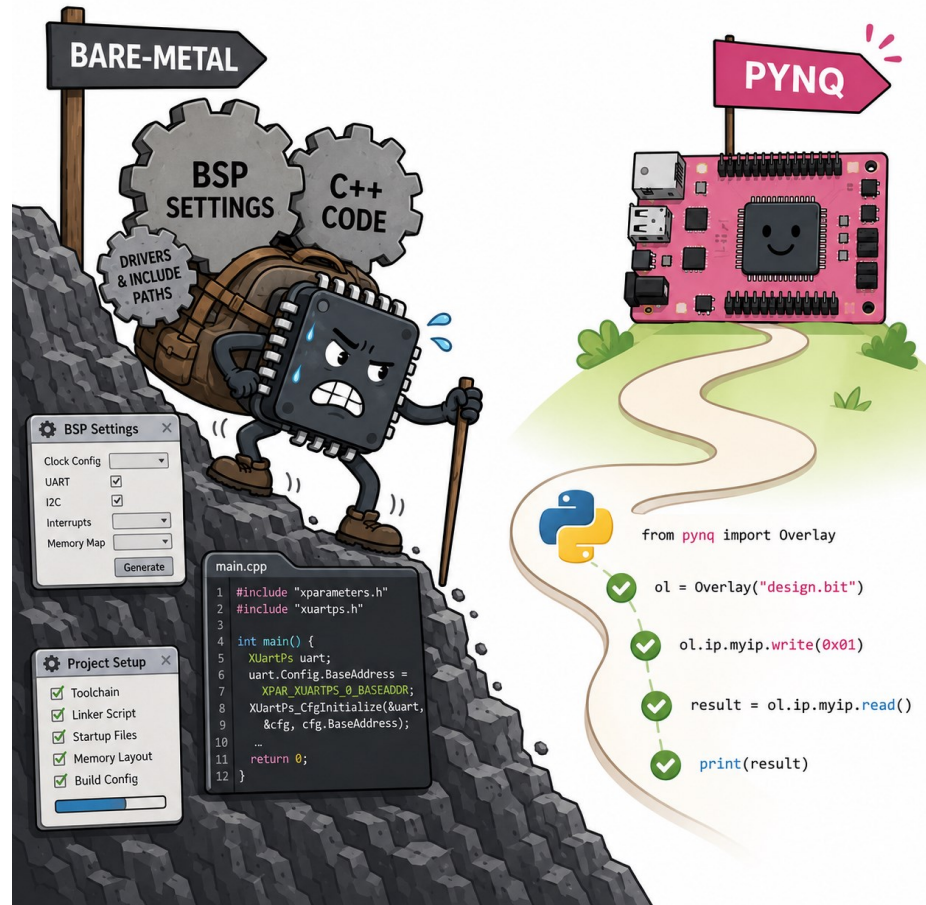
Resource Usage

Resource	Utilization	Available	Utilization %
LUT	8.2k	53.2k	15.5 %
LUTRAM	0.6k	17.4k	3.4 %
FF	12k	106.4k	11 %
BRAM	1	140	1.4 %
DSP	55	220	25 %

System Architecture



System Architecture - CPU



ChatGPT generated

Implementations based on SCAN package:

- PYNQ (python)
 - ease of development
 - prototyping
 - verification
- Bare-Metal (C++)
 - higher performance

SCAN Source-Code



<https://github.com/DLR-KI/scan>

Summary

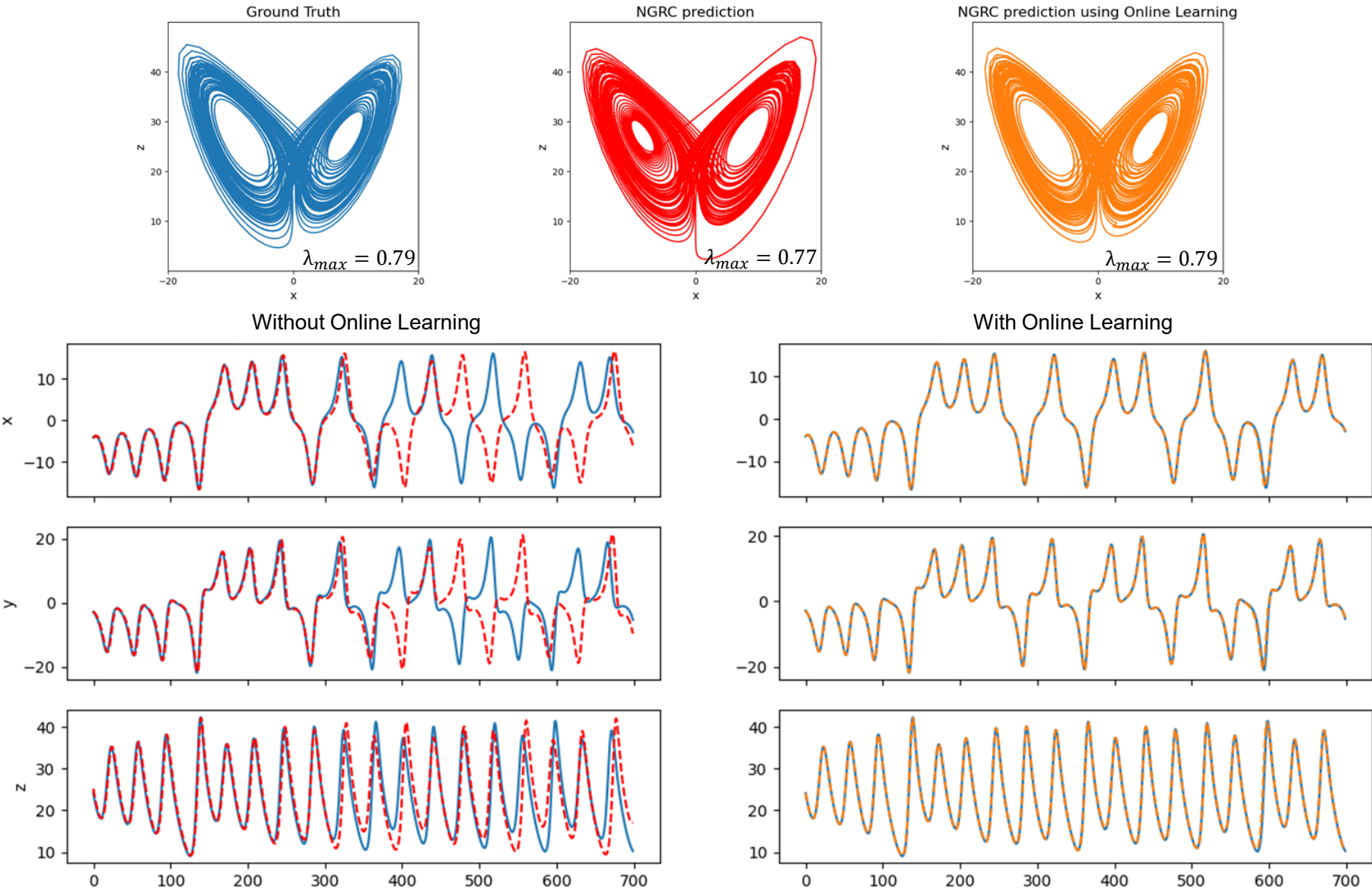
Deployment	Training Latency	Inference Loop Latency	End-to-end Latency
PYNQ (no FPGA)	257 ms	479 ms	749 ms
Bare-Metal (no FPGA)	160 ms	31 ms	192 ms
PYNQ	257 ms	0.68 ms	273 ms
Bare-Metal	160 ms	0.64 ms	162 ms

Train/Inference on 400/200 timesteps

For 1 Hz Telemetry data this is more than enough

Power Estimation	Clock Frequency	Inference Latency
1.776 W	166 MHz	3.147 μ s

Online Learning - Results

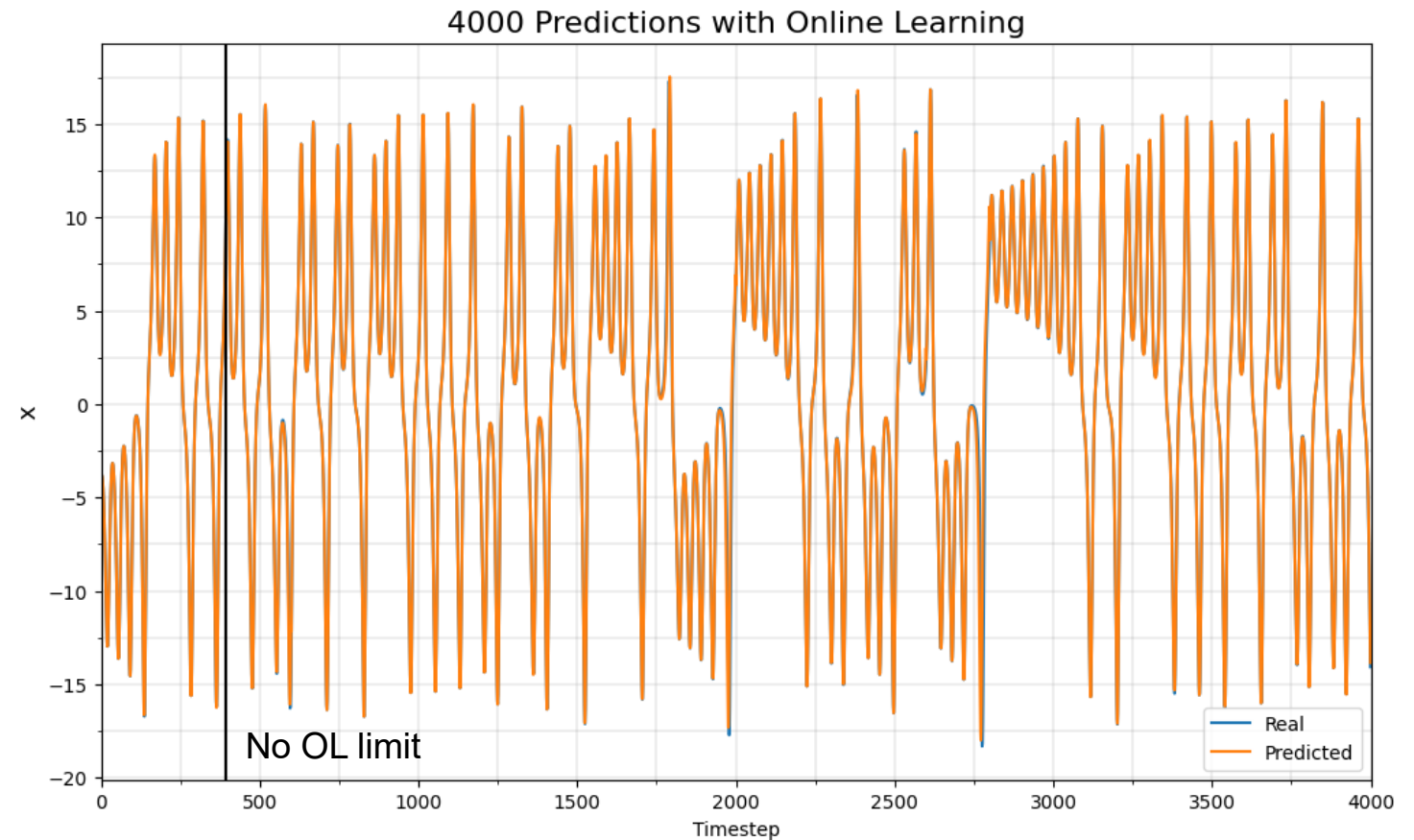


Long-Horizon Forecast

- Previously $*NRMSE_x = 60\%$
- With OL $NRMSE_x = 4\%$

*Normalised Root Mean Squared Error

$$NRMSE = \frac{1}{\max(X)} \sqrt{\frac{\sum_1^N (\text{Predicted}_i - \text{Real}_i)^2}{N}}, \quad N = 4000$$



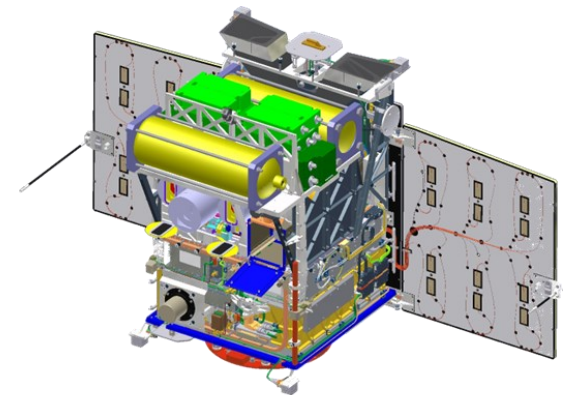
Summary



- Online Learning method of NGRC that significantly extends forecast horizon
- Low-latency SoC architecture where CPU trains – FPGA predicts
- Trivial and interpretable implementation

Outlook

- Expand on hardware optimization (pipelining, quantization, pruning, etc.)
- Benchmark on modern devices (ex. Ultrascale+)
- Finding optimal NGRC embeddings for Telemetry Data for on-board computing



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THANK YOU!
FEEDBACK, QUESTIONS?

