

Nested Tailbiting Convolutional Codes for Secrecy, Privacy, and Storage

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ABSTRACT

The key agreement problem with biometric or physical identifiers and two terminals for key enrollment and reconstruction is considered. A nested convolutional code construction that performs lossy compression with side information is proposed. Nested convolutional codes are an alternative to nested polar codes and nested random linear codes that achieve all points of the key-leakage-storage regions of the generated-secret and chosen-secret models for long block lengths. Our design uses a convolutional code for vector quantization during enrollment and a subcode of it for error correction during reconstruction. Physical identifiers with small bit error probability are considered to illustrate the gains of the proposed construction. One variant of nested convolutional codes improves on all previous constructions in terms of the key vs. storage rate ratio but it has high complexity. Another variant of nested convolutional codes with lower complexity performs similarly to previously designed nested polar codes. The results suggest that the choice of convolutional or polar codes for key agreement with identifiers depends on the complexity constraints.

CCS CONCEPTS

• Security and privacy → Information-theoretic techniques.

KEYWORDS

nested codes, information privacy, tailbiting, convolutional codes, physical unclonable functions

ACM Reference Format:

Thomas Jerkovits, Onur Günlü, Vladimir Sidorenko, and Gerhard Kramer. 2020. Nested Tailbiting Convolutional Codes for Secrecy, Privacy, and Storage. In *2020 ACM Workshop on Information Hiding and Multimedia Security (IH&MMSec'20)*, June 22–24, 2020, Denver, CO, USA. ACM, New York, NY, USA, 11 pages. <https://doi.org/10.1145/3369412.3395063>

1 INTRODUCTION

Irises and fingerprints are biometric identifiers used to authenticate and identify individuals, and to generate secret keys [4]. In a digital

device, there are digital circuits that have outputs unique to the device. One can generate secret keys from such physical unclonable functions (PUFs) by using their outputs as a source of randomness. Fine variations of ring oscillator (RO) outputs, the start-up behavior of static random access memories (SRAM), and quantum-physical readouts through coherent scattering [36] can serve as PUFs that have reliable outputs and high entropy [11, 17]. One can consider them as physical “one-way functions” that are easy to compute and difficult to invert [32].

There are several security, privacy, storage, and complexity constraints that a PUF-based key agreement method should fulfill. First, the method should not leak information about the secret key (negligible *secrecy leakage*). Second, the method should leak as little information about the identifier (minimum *privacy leakage*). The privacy leakage constraint can be considered as an upper bound on the secrecy leakage via the public information of the first enrollment of a PUF about the secret key generated by the second enrollment of the same PUF [12]. Third, one should limit the *storage* rate because storage can be expensive and limited, e.g., for internet-of-things (IoT) device applications. Similarly, the hardware cost, e.g., hardware area, of the encoder and decoder used for key agreement with PUFs should be small for such applications.

There are two common models for key agreement: the *generated-secret (GS)* and the *chosen-secret (CS) models*. An encoder extracts a secret key from an identifier measurement for the GS model, while for the CS model a secret key that is independent of the identifier measurements is given to the encoder by a trusted entity. In the classic key-agreement model introduced in [1] and [30], two terminals observe correlated random variables and have access to a public, authenticated, and one-way communication link; an eavesdropper observes only the public messages called *helper data*. The regions of achievable secret-key vs. privacy-leakage (key-leakage) rates for the GS and CS models are given in [18, 25]. The storage rates for general (non-negligible) secrecy-leakage levels are analyzed in [22], while the rate regions with multiple encoder and decoder measurements of a hidden source are treated in [15]. There are other key-agreement models with an eavesdropper that has access to a sequence correlated with the identifier outputs, e.g., in [6, 8, 12, 21]. This model is not realistic for PUFs, unlike physical-layer security primitives and some biometric identifiers that are continuously available for physical attacks. PUFs are used for *on-demand* key reconstruction, i.e., the attack should be performed during execution, and an invasive attack applied to obtain a correlated sequence permanently changes the identifier output [11]. Therefore, we assume

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IH&MMSec '20, June 22–24, 2020, Denver, CO, USA

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ACM ISBN 978-1-4503-7050-9/20/06...\$15.00

<https://doi.org/10.1145/3369412.3395063>

that the eavesdropper cannot obtain a sequence correlated with the PUF outputs.

Two classic code constructions for key agreement are code-offset fuzzy extractors (COFE) [10] and the fuzzy commitment scheme (FCS) [20], which are based on a one-time padding step in combination with an error correcting code. Both constructions require a storage rate of 1 bit/symbol due to the one-time padding step. A Slepian-Wolf (SW) [37] coding method, which corresponds to syndrome coding for binary sequences, is proposed in [5] to reduce the storage rate so that it is equal to the privacy-leakage rate. It is shown in [13] that these methods do not achieve the key-leakage-storage boundaries of the GS and CS models.

Wyner-Ziv (WZ) [41] coding constructions that bin the observed sequences are shown in [13] to be optimal deterministic code constructions for key agreement with PUFs. Nested random linear codes are shown to asymptotically achieve boundary points of the key-leakage-storage region. A second WZ-coding construction uses a nested version of polar codes (PCs) [3], which are designed in [13] for practical SRAM PUF parameters to illustrate that rate tuples that cannot be achieved by using previous code constructions can be achieved by nested PCs.

A closely related problem to the key agreement problem is Wyner's wiretap channel (WTC) [40]. The main aim in the WTC problem is to hide a transmitted message from the eavesdropper that observes a channel output correlated with the observation of a legitimate receiver. There are various code constructions for the WTC that achieve the secrecy capacity, e.g., in [2, 24, 27, 29], and some of these constructions use nested PCs, e.g., [2, 27]. Similarly, nested PCs are shown in [7] to achieve the strong coordination capacity boundaries, defined and characterized in [9].

We design codes for key agreement with PUFs by constructing nested convolutional codes. Due to the broad use of nested codes in, e.g., WTC and strong coordination problems, the proposed nested convolutional code constructions can be useful also for these problems. A summary of the main contributions is as follows.

- We propose a method to obtain nested tailbiting convolutional codes (TBCCs) that are used as a WZ-coding construction, which is a binning method used in various achievability schemes and can be useful for various practical code constructions.
- We develop a design procedure for the proposed nested convolutional code construction adapted to the problem of key agreement with biometric or physical identifiers. This is an extension of the asymptotically optimal nested code constructions with random linear codes and PCs proposed in [13]. We consider binary symmetric sources and binary symmetric channels (BSCs). Physical identifiers such as RO PUFs with transform coding [14] and SRAM PUFs [28] are modeled by these sources and channels.
- We design and simulate nested TBCCs for practical source and channel parameters obtained from the best PUF design in the literature. The target block-error probability is $P_B = 10^{-6}$ and the target secret-key size is 128 bits. We illustrate that one variant of nested codes achieves the largest key vs. storage rate ratio but it has high decoding complexity. Another variant of nested codes with lower decoding complexity

achieves a rate ratio that is slightly greater than the rate ratio achieved by a nested PC. We also illustrate the gaps to the finite-length bounds.

This paper is organized as follows. In Section 3, we describe the GS and CS models, and give their rate regions that are also evaluated for binary symmetric sequences. We summarise in Section 4 our new nested code construction that uses convolutional codes. In Section 5, we propose a design procedure for the new nested TBCCs adapted to the key agreement with PUFs problem. Section 6 compares the estimated decoding complexity of TBCCs and PCs. Section 7 illustrates the significant gains from nested convolutional codes designed for practical PUF parameters as compared to previously-proposed nested PCs and other channel codes in terms of the key vs. storage rate ratio.

2 PRELIMINARIES

2.1 Notation

Let \mathbb{F}_2 denote the finite field of order 2 and let $\mathbb{F}_2^{a \times b}$ denote the set of all $a \times b$ matrices over \mathbb{F}_2 . Rows and columns of $a \times b$ matrices are indexed by $1, \dots, a$ and $1, \dots, b$, and $h_{i,j}$ is the element in the i -th row and j -th column of a matrix \mathbf{H} . \mathbb{F}_2^a denotes the set of all row vectors of length a over \mathbb{F}_2 . With $\mathbf{0}_{a \times b}$ we denote the all-zero matrix of size $a \times b$. A linear block code over \mathbb{F}_2 of length N and dimension K is a K -dimensional subspace of \mathbb{F}_2^N and denoted by (N, K) . A variable with superscript denotes a string of variables, e.g., $X^n = X_1 \dots X_i \dots X_n$, and a subscript denotes the position of a variable in a string. A random variable X has probability distribution P_X . Calligraphic letters such as \mathcal{X} denote sets, and set sizes are written as $|\mathcal{X}|$. $\text{Enc}(\cdot)$ is an encoder mapping and $\text{Dec}(\cdot)$ is a decoder mapping. $H_b(x) = -x \log x - (1-x) \log(1-x)$ is the binary entropy function, where we take logarithms to the base 2. The $*$ -operator is defined as $p*x = p(1-x) + (1-p)x$. A BSC with crossover probability p is denoted by $\text{BSC}(p)$. $X^n \sim \text{Bern}^n(\alpha)$ is an independent and identically distributed (i.i.d.) binary sequence of random variables with $\Pr[X_i = 1] = \alpha$ for $i = 1, 2, \dots, n$. \mathbf{H}^T represents the transpose of the matrix \mathbf{H} . Drawing an element e from a set \mathcal{E} uniformly at random is denoted by

$$e \stackrel{\$}{\leftarrow} \mathcal{E}. \quad (1)$$

2.2 Convolutional Codes

Denote the parameters of a block code generated by a binary convolutional encoder as (N, K) , where N is the blocklength and K is the code dimension (in bits). At each time step, the convolutional encoder receives k input bits and generates n output bits. The number of clock cycles needed to encode K bits is $\ell = \frac{K}{k}$. We consider convolutional encoders with a single shift register only. The shift register consists of m delay cells, where m is also called the memory of the encoder. The bit value stored in the i -th delay cell at time step t is denoted by $s_t^{(i)} \in \mathbb{F}_2$ for $i = 1, \dots, m$. For a given binary input vector $\mathbf{u}_t = (u_t^{(1)}, u_t^{(2)}, \dots, u_t^{(k)})$ of length k at time step t , the encoder outputs a binary vector $\mathbf{c}_t = (c_t^{(1)}, c_t^{(2)}, \dots, c_t^{(n)})$ of length n . The encoder can be described by the state-space representation

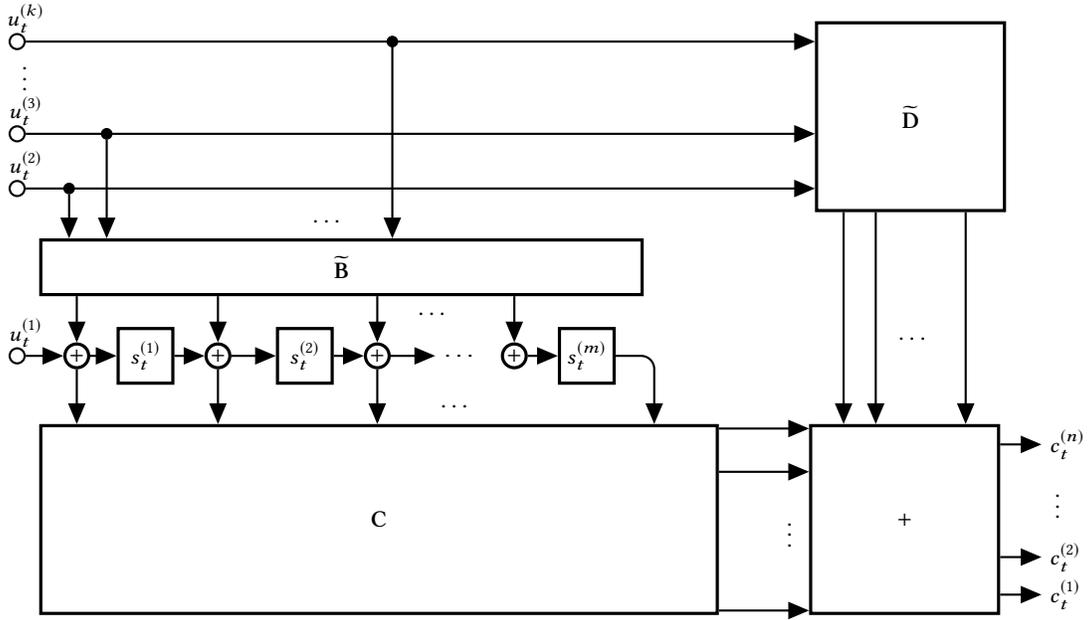


Figure 1: Encoder circuit of convolutional codes described in Section 2.2.

of the encoder circuit such that the output c_t is

$$c_t = s_t \cdot C^T + u_t \cdot D^T \quad (2)$$

where $s_t = (s_t^{(1)}, s_t^{(2)}, \dots, s_t^{(m)})$ is the vector describing the content of the shift register, $C \in \mathbb{F}_2^{n \times m}$ is the observation matrix, and $D \in \mathbb{F}_2^{n \times k}$ is the transition matrix. The content of the shift register for the next clock cycle at time step $t + 1$ is then

$$s_{t+1} = s_t \cdot A^T + u_t \cdot B^T \quad (3)$$

where $A \in \mathbb{F}_2^{m \times m}$ is the system matrix and $B \in \mathbb{F}_2^{m \times k}$ is the control matrix. For the case of a single shift register we have that the system matrix is given by

$$A = \begin{bmatrix} \mathbf{0}_{1 \times (m-1)} & 0 \\ \mathbf{I}_{(m-1) \times (m-1)} & \mathbf{0}_{(m-1) \times 1} \end{bmatrix} \quad (4)$$

where $\mathbf{I}_{(m-1) \times (m-1)} \in \mathbb{F}_2^{(m-1) \times (m-1)}$ is the identity matrix. For simplicity, first entry of the input tuple $u_t^{(1)}$ is always an input to the shift register and thus we can write $B = (\mathbf{e}_1^T | \tilde{B})$ and $D = (\mathbf{0}_{n \times 1} | \tilde{D})$, where \mathbf{e}_1 is the unit row vector having a 1 in the first position and 0 everywhere else, $\tilde{B} \in \mathbb{F}_2^{m \times (k-1)}$, and $\tilde{D} \in \mathbb{F}_2^{n \times (k-1)}$. The corresponding encoder circuit is shown in Figure 1. Elements of a vector entering a square box, which represents one of the aforementioned matrices, depicts a vector-matrix multiplication, and the box with the addition symbol depicts an elementwise vector-vector addition. Therefore, the encoder of the convolutional code can be described by the three matrices \tilde{B} , C , and \tilde{D} . We denote such an encoder by $[\tilde{B}, C, \tilde{D}]$.

Using the tailbiting method from [19, Chapter 4.8], we avoid having a rate loss, unlike the zero-tail termination method. We have $N = \ell n$ and the resulting code rate is $R = \frac{k}{n}$. A *tailbiting convolutional code* (TBCC) can be represented by a tailbiting trellis using

ℓ sections and 2^m states per section. The codewords correspond to all possible paths in the trellis, where starting and ending states coincide. TBCCs can be decoded by using the wrap around Viterbi algorithm (WAVA) [35]. This decoder is suboptimal but performs close to the performance of the maximum likelihood decoder.

Let A_d be the number of codewords of Hamming weight d for $d = 0, 1, \dots, N$, which characterizes the distance spectrum of a TBCC. The weight enumerator polynomial $A(X)$ is then defined as

$$A(X) \stackrel{\text{def}}{=} \sum_{d=0}^N A_d X^d. \quad (5)$$

To compute the weight enumerator and to determine the distance spectrum we use the approach described in [39]. Consider the state transition matrix $T(X)$ of size $2^m \times 2^m$, where every entry $t_{i,j}(X)$ is either X^d , where d is the Hamming weight of the output produced by the encoder when going from the state labeled with i to the state labeled with j , or 0 if there is no possible transition between the aforementioned states. Therefore, we have

$$A(X) = \text{Tr} \left(T^\ell(X) \right) \quad (6)$$

where $T^\ell(X)$ denotes multiplication of the matrix $T(X)$ with itself ℓ times and $\text{Tr}(\cdot)$ denotes the trace.

3 PROBLEM FORMULATION

Consider the GS model in Figure 2(a), where a biometric or physical source output is used to generate a secret key. The source X , noisy measurement \mathcal{Y} , secret key \mathcal{S} , and storage \mathcal{W} alphabets are finite sets. During enrollment, the encoder observes the i.i.d. identifier output X^N , generated according to some P_X , and computes a secret key $S \in \mathcal{S}$ and public helper data $W \in \mathcal{W}$ as $(S, W) = \text{Enc}(X^N)$. During reconstruction, the decoder observes a noisy source measurement

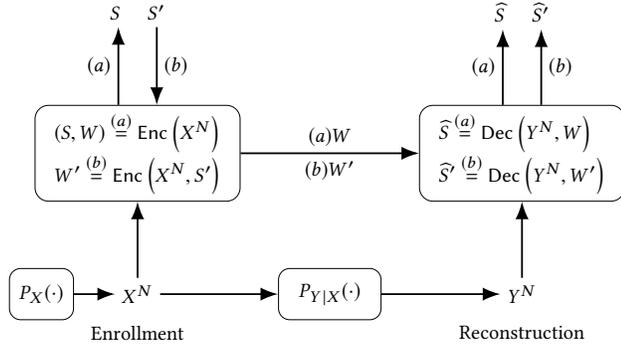


Figure 2: The (a) GS and (b) CS models.

Y^N of the source output X^N through a memoryless measurement channel $P_{Y|X}$ in addition to the helper data W . The decoder estimates the secret key as $\widehat{S} = \text{Dec}(Y^N, W)$. Furthermore, Figure 2(b) shows the CS model, where a secret key $S' \in \mathcal{S}$ is embedded into the helper data as $W' = \text{Enc}(X^N, S')$. The decoder for the CS model estimates the secret key as $\widehat{S}' = \text{Dec}(Y^N, W')$.

Definition 3.1. A key-leakage-storage tuple (R_s, R_ℓ, R_w) is *achievable* for the GS and CS models if, given any $\epsilon > 0$, there is some $N \geq 1$, an encoder, and a decoder such that $R_s = \frac{\log |\mathcal{S}|}{N}$ and

$$P_B \stackrel{\text{def}}{=} \Pr[\widehat{S} \neq S] \leq \epsilon \quad (\text{reliability}) \quad (7)$$

$$\frac{1}{N} I(S; W) \leq \epsilon \quad (\text{secrecy}) \quad (8)$$

$$\frac{1}{N} H(S) \geq R_s - \epsilon \quad (\text{key uniformity}) \quad (9)$$

$$\frac{1}{N} \log |\mathcal{W}| \leq R_w + \epsilon \quad (\text{storage}) \quad (10)$$

$$\frac{1}{N} I(X^N; W) \leq R_\ell + \epsilon \quad (\text{privacy}) \quad (11)$$

where for the CS model, S and W in the constraints should be replaced by, respectively, S' and W' .

The *key-leakage-storage* regions \mathcal{R}_{gs} and \mathcal{R}_{cs} for the GS and CS models, respectively, are the closures of the sets of achievable tuples for the corresponding models. \diamond

THEOREM 3.2 ([18]). *The key-leakage-storage region \mathcal{R}_{gs} for the GS model is the union of the bounds*

$$0 \leq R_s \leq I(U; Y) \quad (12)$$

$$R_\ell \geq I(U; X) - I(U; Y) \quad (13)$$

$$R_w \geq I(U; X) - I(U; Y) \quad (14)$$

over all $P_{U|X}$ such that $U - X - Y$ form a Markov chain. Similarly, the key-leakage-storage region \mathcal{R}_{cs} for the CS model is the union of the bounds in (12), (13), and

$$R_w \geq I(U; X). \quad (15)$$

These regions are convex sets. The alphabet \mathcal{U} of the auxiliary random variable U can be limited to have size $|\mathcal{U}| \leq |\mathcal{X}| + 1$. Deterministic encoders and decoders suffice to achieve these regions.

Suppose the transform-coding algorithms proposed in [14] are applied to RO PUFs or any PUF circuits with continuous-valued outputs to obtain X^N that is almost i.i.d. according to a uniform Bernoulli random variable, i.e., $X^N \sim \text{Bern}^N(\frac{1}{2})$, and the channel $P_{Y|X}$ is a BSC(p_A) for $p_A \in [0, 0.5]$. The key-leakage-storage region $\mathcal{R}_{\text{gs,bin}}$ of the GS model for this case is the union of the bounds

$$\begin{aligned} 0 &\leq R_s \leq 1 - H_b(q * p_A) \\ R_\ell &\geq H_b(q * p_A) - H_b(q) \\ R_w &\geq H_b(q * p_A) - H_b(q) \end{aligned} \quad (16)$$

over all $q \in [0, 0.5]$ [18], which follows by using an auxiliary random variable U such that $P_{X|U} \sim \text{BSC}(q)$ due to Mrs. Gerber's lemma [41]. The rate tuples on the boundary of the region $\mathcal{R}_{\text{gs,bin}}$ are uniquely defined by the ratio $\frac{R_s}{R_w}$. We therefore use this ratio as the metric to compare our nested TBCCs with previously-proposed nested PCs and channel codes. A larger key vs. storage rate ratio suggests that the code construction is closer to an achievable point that is on the boundary of the region $\mathcal{R}_{\text{gs,bin}}$, which is an optimal tuple. We next focus on the GS model for code constructions. All results can be extended to the CS model by using an additional one-time padding step [12].

4 NESTED CONVOLUTIONAL CODE CONSTRUCTION

In this section, we sketch the main steps to obtain a nested construction for convolutional codes. Furthermore, we give two explicit algorithms to find good code constructions. The first algorithm addresses the search of a good error correcting code (N, K_s) , denoted by C_s , and the second algorithm finds a (N, K_q) code C_q used as a vector quantizer such that C_s is a subcode of C_q , i.e., $C_s \subseteq C_q$.

4.1 Nested Convolutional Codes

Using the encoder circuit depicted in Figure 1, we construct two codes C_q and C_s such that $C_s \subseteq C_q$. Let C_q be the (N, K_q) TBCC with memory m and $K_q = \ell k_q$ generated by using the encoder defined by the matrices $[\widetilde{\mathbf{B}}, \widetilde{\mathbf{C}}, \widetilde{\mathbf{D}}]$. Recall that $\mathbf{B} = (\mathbf{e}_1^T | \widetilde{\mathbf{B}})$ with $\widetilde{\mathbf{B}} \in \mathbb{F}_2^{m \times (k_q - 1)}$ and $\mathbf{D} = (\mathbf{0}^T | \widetilde{\mathbf{D}})$ with $\widetilde{\mathbf{D}} \in \mathbb{F}_2^{n \times (k_q - 1)}$. By removing the i -th column of $\widetilde{\mathbf{B}}$ and $\widetilde{\mathbf{D}}$ simultaneously, one obtains a new encoder that generates a code of rate $\frac{k_q - 1}{n}$, which is a subcode of the original code. This is true, since the new code corresponds to all codewords by encoding the original code but restricting to all inputs where $u_t^{(i)} = 0$. By “freezing” further input bits we can therefore obtain a subcode of rates

$$R_s = \frac{1}{n}, \frac{2}{n}, \dots, \frac{k_q - 1}{n}. \quad (17)$$

To obtain codes with rates of better granularity between $\frac{1}{n}$ and $\frac{k_q - 1}{n}$ that are not in (17), we can freeze input bits in a time-variant manner. That is, by using the encoder ℓ times, we can freeze a different amount of input bits in different clock cycles. This allows to obtain codes of rates

$$R_s = \frac{\ell}{N}, \frac{\ell + 1}{N}, \dots, \frac{K_q}{N}. \quad (18)$$

Algorithm 1: Search for (N, K_s) TBCC $C_s, R_s = \frac{1}{n}$

Input : n, m, K_s, P_B, W_{\max} (maximum number of iterations)
Output : $C \in \mathbb{F}_2^{n \times m}$

- 1 **Initialize:**
- 2 $p_c \leftarrow 0$
- 3 $C \leftarrow \mathbf{0}$
- 4 **for** $w \leftarrow 1$ **to** W_{\max} **do**
- 5 $C' \xleftarrow{\$} \mathbb{F}_2^{n \times m}$
- 6 Compute A_d for the (N, K_s) TBCC generated by $[0, C', \mathbf{0}]$ for $d = 0, \dots, N$ using (5) and (6)
- 7 Find p'_c such that: $P_B^{\text{UB}}(A_d, p'_c) = P_B$
- 8 **if** $p'_c \geq p_c$ **then**
- 9 $p_c \leftarrow p'_c$
- 10 $C \leftarrow C'$
- 11 **return** C

Denote the parameters of the subcode, obtained by freezing input bits accordingly, as (N, K_s) . Note that by freezing input bits in a time-variant manner, K_s is not necessarily a multiple of ℓ . Furthermore, the procedure can be applied also to add columns to $\tilde{\mathbf{B}}$ and $\tilde{\mathbf{D}}$ to generate a supercode. The design procedure of the nested convolutional code construction is split into two steps:

- (1) Search for a good error correcting code C_s of rate $R_s = \frac{1}{n} = \frac{K_s}{N}$ at given target block error probability P_B by finding an appropriate matrix C .
- (2) Expand the low rate code by finding appropriate matrices $\tilde{\mathbf{B}}$ and $\tilde{\mathbf{D}}$ to obtain a good code of rate $R_q = \frac{k_q}{n} = \frac{K_q}{N}$ that achieves a low average distortion q .

Note that for the first step we restrict to codes of rate $R_s = \frac{1}{n}$ and hence the matrices $\tilde{\mathbf{B}}$ and $\tilde{\mathbf{D}}$ are vanishing. The first step can also be performed for codes of any rate $R_s > \frac{1}{n}$, but then also the appropriate matrices $\tilde{\mathbf{B}}$ and $\tilde{\mathbf{D}}$ have to be found accordingly.

4.2 Design of a Convolutional Code for Error Correction

For fixed parameters n, m , and K_s , we try to find a matrix C such that the resulting (N, K_s) TBCC C_s at a given target block error probability P_B can be operated on a noisy BSC with large crossover probability p_c . To evaluate P_B we use the union bound, see, e.g., [33], and the distance spectrum of the code. This gives an upper bound on P_B under maximum likelihood decoding. The bound is given by

$$P_B \leq P_B^{\text{UB}}(A_d, p_c) \stackrel{\text{def}}{=} \sum_{d=d_{\min}}^N A_d \sum_{i=\lceil d/2 \rceil}^d \binom{d}{i} p_c^i (1-p_c)^{d-i} \quad (19)$$

where d_{\min} is the minimum distance of the code.

Algorithm 2: Search for (N, K_q) TBCC $C_q, R_q = \frac{k_q}{n}$

Input : $m, k_q, k_s, W_{\max}, C, \tilde{\mathbf{B}}_s \in \mathbb{F}_2^{m \times (k_s-1)}, \tilde{\mathbf{D}}_s \in \mathbb{F}_2^{n \times (k_s-1)}$
Output : $\tilde{\mathbf{B}}_q \in \mathbb{F}_2^{m \times (k_q-1)}, \tilde{\mathbf{D}}_q \in \mathbb{F}_2^{n \times (k_q-1)}$

- 1 **Initialize:**
- 2 $\tilde{\mathbf{B}}_q \leftarrow (\tilde{\mathbf{B}}_s | \mathbf{0})$
- 3 $\tilde{\mathbf{D}}_q \leftarrow (\tilde{\mathbf{D}}_s | \mathbf{0})$
- 4 $d \leftarrow 0$
- 5 $A \leftarrow 0$
- 6 **for** $w \leftarrow 1$ **to** W_{\max} **do**
- 7 $\mathbf{B}' \xleftarrow{\$} \mathbb{F}_2^{m \times (k_q - k_s)}$
- 8 $\mathbf{D}' \xleftarrow{\$} \mathbb{F}_2^{n \times (k_q - k_s)}$
- 9 $\tilde{\mathbf{B}}'_q \leftarrow (\tilde{\mathbf{B}}_s | \mathbf{B}')$
- 10 $\tilde{\mathbf{D}}'_q \leftarrow (\tilde{\mathbf{D}}_s | \mathbf{D}')$
- 11 Compute d_{free} and A_{free} for $[\tilde{\mathbf{B}}'_q, C, \tilde{\mathbf{D}}'_q]$
- 12 **if** $d_{\text{free}} > d$ **or** ($d_{\text{free}} = d$ **and** $A_{\text{free}} < A$) **then**
- 13 $d \leftarrow d_{\text{free}}$
- 14 $A \leftarrow A_{\text{free}}$
- 15 $\tilde{\mathbf{B}}_q \leftarrow \tilde{\mathbf{B}}'_q$
- 16 $\tilde{\mathbf{D}}_q \leftarrow \tilde{\mathbf{D}}'_q$
- 17 **return** $\tilde{\mathbf{B}}_q, \tilde{\mathbf{D}}_q$

The design of the code C_s is performed by a purely random search of the matrix C as described in Algorithm 1. This algorithm searches the best TBCC of rate $R_s = \frac{1}{n}$ by randomly generating different matrices C . The matrix C of the code that yields the largest p_c at a given target block error probability P_B is returned as the output of Algorithm 1.

4.3 Design of a Convolutional Code for Vector Quantization

In this section, an algorithm to obtain a high rate code from an existing low rate convolutional encoder is explained. The algorithm is presented in Algorithm 2. The inputs are the system matrix, the observation matrix, and the transition matrix of the low rate code with rate

$$R_s = \frac{k_s}{n}. \quad (20)$$

By randomly adding $k_q - k_s$ columns to both, the system and the transition matrix of a code of high rate

$$R_q = \frac{k_q}{n} \quad (21)$$

is constructed. The algorithm performs a random search and returns the best configuration. As selection metrics, the free distance and its multiplicity are chosen. The free distance d_{free} of a convolutional code is defined as the minimum Hamming weight between any two differing paths in the state transition diagram [19, Chapter 3]. Due to linearity of convolutional codes, d_{free} is also the minimum Hamming weight over the nonzero paths. We denote by A_{free} the

multiplicity of paths that have Hamming weight d_{free} . To find a good high rate code, we use d_{free} and A_{free} to select the best encoder. The BEAST algorithm described in [19, Chapter 10] is a fast method to compute d_{free} and A_{free} . The selection criterion is as follows: Keep the code with largest d_{free} and in case of a tie decide for the code with smaller A_{free} .

5 DESIGN OF NESTED CONVOLUTIONAL CODES FOR PUFs

Algorithms 1 and 2 are combined to find good nested code constructions for the coding problem described in Section 3. Two TBCCs C_s and C_q of the same length N are needed such that $C_s \subseteq C_q$. Let K_q and K_s denote the dimensions of C_q and C_s , respectively, and let $R_q = \frac{K_q}{N}$ and $R_s = \frac{K_s}{N}$ denote their code rates. The objective is to maximize the key vs. storage rate ratio. Since $R_s = \frac{K_s}{N}$ and $R_w = \frac{K_q - K_s}{N}$, we have

$$\frac{R_s}{R_w} = \frac{K_s}{K_q - K_s} = \left(\frac{R_q}{R_s} - 1 \right)^{-1}. \quad (22)$$

Therefore, we maximize R_s and minimize R_q simultaneously.

To reconstruct the key S of size K_s (in bits) the code C_s has to correct errors on the artificial BSC channel with crossover probability $p_c = q * p_A$ at a given target P_B . The code C_q serves as a vector quantizer with average distortion q such that [13]

$$q \leq \frac{p_c - p_A}{1 - 2p_A}. \quad (23)$$

The design procedure is then as follows:

- (1) Choose m and n to design a TBCC of rate $R_s = \frac{1}{n}$ by using Algorithm 1.
- (2) Obtain the corresponding value of p_c where the code achieves the target block error probability P_B by Monte Carlo simulations.
- (3) Construct code C_q from C_s by using Algorithm 2 such that (23) is satisfied.

The last step in this procedure is executed by applying Algorithm 2 incrementally as follows:

- (1) Initialization: Start constructing a code $C_q^{(0)}$ of rate $R_q^{(0)} = \frac{2}{n}$ from code C_s (Algorithm 1).
- (2) Set $i \leftarrow 1$.
- (3) Construct a code $C_q^{(i)}$ of rate $R_q^{(i)} = \frac{i+2}{n}$ from code $C_q^{(i-1)}$ (Algorithm 2).
- (4) If the average distortion achieved by the code $C_q^{(i)}$ satisfies the constraint given in (23), stop; else increment $i \leftarrow i + 1$ and go to step (3).

The final code C_q is the code in the last iteration. To obtain code rates in between those steps we randomly freeze inputs of the encoder in a time-variant manner as described in Section 4. Since in each iteration the code is optimized for the minimum distance of the code, we can only freeze inputs on the last added input. This way we guarantee to preserve the minimum distance of the code for the next iteration due to $C_q^{(i-1)} \subseteq C_q^{(i)}$.

6 ESTIMATED DECODING COMPLEXITY

We compare the decoding complexities of TBCCs and PCs. Since the real complexity of decoding depends on the hardware implementation, we only estimate the complexity for both code classes by using standard decoding algorithms.

The WAVA algorithm performs standard Viterbi decoding on the tailbiting trellis of the TBCC in a circular fashion. That means the decoder runs over the trellis several times and at each iteration the probabilities of the starting states of the trellis are updated according to the probabilities of the ending states of the previous iteration. Therefore, the WAVA algorithm scales with the complexity of a standard Viterbi decoder times the number of iterations. For simplicity, we consider the worst case complexity and hence let V denote the number of maximum iterations of the WAVA decoder.

According to [26], let κ be the complexity of a standard Viterbi decoder with indices

- F for Forney trellis,
- P for precomputation,
- M for merged or minimal trellis.

We have for the total of number $\frac{N}{n}$ of trellis sections

$$\kappa_F \propto N \cdot 2^{k+m} \quad (24)$$

$$\kappa_P \propto \frac{N}{n} \left(2^{k+m} + 2^n \right) \quad (25)$$

$$\kappa_M \propto N \cdot 2^{\min\{k, n-k\}+m}. \quad (26)$$

By scaling these complexities with the maximum number of WAVA iterations V we obtain the desired complexities of decoding a TBCC. For decoding on the Forney trellis, we can reuse the branch metrics computed in the first WAVA iteration in the following $V - 1$ iterations and; therefore, we obtain

$$\kappa_F^{\text{WAVA}} \propto (n + V - 1) \frac{N}{n} 2^{k+m} \quad (27)$$

$$\kappa_P^{\text{WAVA}} \propto \frac{N}{n} \left(V \cdot 2^{k+m} + 2^n \right) \quad (28)$$

$$\kappa_M^{\text{WAVA}} \propto V N \cdot 2^{\min\{k, n-k\}+m}. \quad (29)$$

Overall we have that the complexity κ^{WAVA} of decoding a TBCC is

$$\kappa^{\text{WAVA}} \propto \min \left\{ \kappa_F^{\text{WAVA}}, \kappa_P^{\text{WAVA}}, \kappa_M^{\text{WAVA}} \right\}. \quad (30)$$

For error correction and vector quantization, we obtain different complexities since we have different values for k . For the error correcting code we have $k = k_s = 1$ and for the vector quantizer code we have $k = k_q$, where k_q is the largest value needed to achieve a rate of R_q such that $k_q = \lceil nR_q \rceil$. The complexity of the vector quantizer can be reduced by considering decoding over the trellis with the time-variant frozen input bit values, since all branches that do not correspond to the frozen input bit value can be removed. For simplicity, we will only consider the complexity over the time-invariant trellis.

For the PCs under successive cancellation list (SCL) decoding [38] with a list size L , we have a complexity proportional to $LN \log_2 N$. This complexity is independent of the code rate and thus applies to C_s and C_q . All decoding complexities are summarized in Table 1.

Note that for the Viterbi decoder parallelization up to a factor of 2^m can be easily achieved since all state nodes in a trellis section

Table 1: Complexities of the error correcting code C_s and vector quantizer code C_q for PCs and TBCCs.

Code class	Complexity of C_s	Complexity of C_q
TBCC κ_F^{WAVA}	$\propto (n + V - 1) \frac{N}{n} 2^m$	$\propto (n + V - 1) \frac{N}{n} 2^{k_q+m}$
TBCC κ_P^{WAVA}	$\propto \frac{N}{n} (V \cdot 2^m + 2^n)$	$\propto \frac{N}{n} (V \cdot 2^{k_q+m} + 2^n)$
TBCC κ_M^{WAVA}	$\propto VN \cdot 2^{1+m}$	$\propto VN \cdot 2^{\min\{k_q, n-k_q\}+m}$
PC	$\propto LN \log_2 N$	$\propto LN \log_2 N$

can be processed independently. For the SCL decoding of PCs, parallelization cannot be achieved without changing the decoder’s error correction performance since each decoded bit sequentially depends on the previously decoded ones.

7 PERFORMANCE EVALUATIONS FOR PUFs

In this section, the performance of TBCCs designed by the proposed procedure for the PUF setting is presented. We consider PUF devices with $p_A = 0.0149$, target block error probability $P_B = 10^{-6}$ and a key size of $K_s = 128$ bits. These values correspond to the best RO PUF designs in the literature [16]. We construct TBCCs with rates $R_s = \frac{1}{3}$ and $R_s = \frac{1}{4}$, and with memories $m = 8$ and $m = 11$. As a reference, we also give a PC construction using the approach described in [13]. Without puncturing we can only provide a PC construction for the case of $R_s = \frac{1}{4}$, since for a key size of $K_s = 128$ and code rate $R_s = \frac{1}{3}$ we would have $N = 384$ which is not a power of two. All simulations for the PCs are performed by using SCL decoding with a list size of $L = 8$. We also compute the results for the rate $\frac{1}{8}$ PC presented in [13] but now for $p_A = 0.0149$ and give the resulting key vs. storage rate ratio $\frac{R_s}{R_w}$. All simulations for the TBCCs are performed by using the WAVA algorithm with a maximum of $V = 4$ iterations. The final results of all discussed codes are given in Table 2.

7.1 Error Correction Performance

The construction of the nested code design starts with the error correcting code C_s . We design two TBCCs with $R_s = \frac{1}{3}$ and $R_s = \frac{1}{4}$ by using Algorithm 1 with $W_{\max} = 10^4$. Results of the Monte Carlo simulations as well as the bound (19) are shown in Figures 3 and 4 for the two TBCCs.

To bound the code performance on a BSC for a given block length and code rate we use two finite length bounds, namely the *meta converse (MC)* and the *random coding union (RCU)* bound from [34]. The MC gives a lower bound and the RCU an upper bound on the block error probability. For $R_s = \frac{1}{4}$, we observe that the TBCC with $m = 11$ outperforms the PC, whereas the TBCC with $m = 8$ performs worse. We also observe that for all considered codes there is still a gap to the finite length bounds.

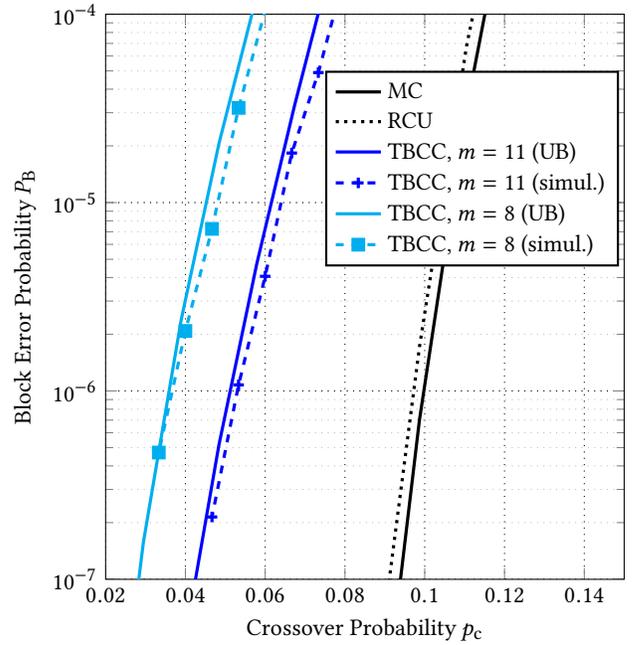


Figure 3: Error correcting performance of different codes with $K_s = 128$ bits and $R_s = \frac{1}{3}$ over a BSC with crossover probability p_c . The MC and RCU bounds for the same code parameters are given as references.

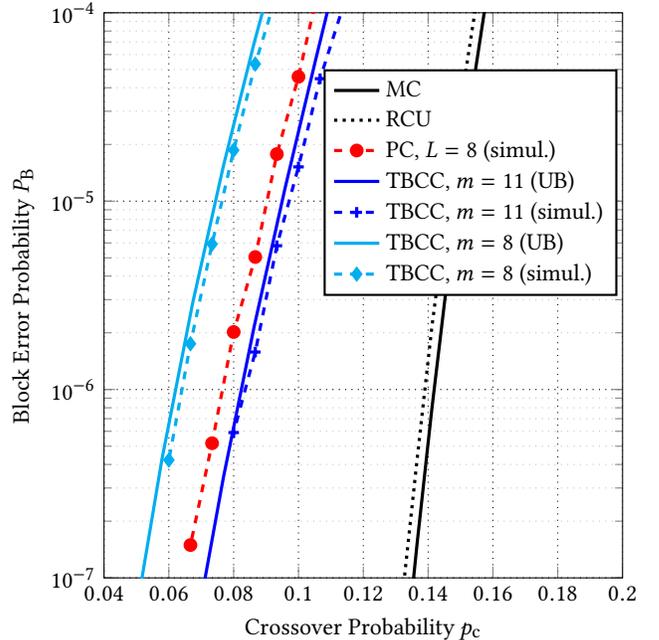


Figure 4: Error correcting performance of different codes with $K_s = 128$ bits and $R_s = \frac{1}{4}$ over a BSC with crossover probability p_c . The MC and RCU bounds for the same code parameters are given as references.

Table 2: Parameters of the designed codes for $K_s = 128$ bits, $p_A = 0.0149$ and $P_B \leq 10^{-6}$ and complexities for C_s and C_q , respectively. For the TBCCs also the type of complexity ($\kappa_F^{\text{WAVA}}, \kappa_M^{\text{WAVA}}$ or κ_P^{WAVA}) which is minimal is given. $\lceil \log_2 |W| \rceil$ is the amount of helper data in bits.

Code	m	R_s	p_c	\bar{q}	R_q	R_w	$\lceil \log_2 W \rceil$	$\frac{R_s}{R_w}$	Complexity C_s	Complexity C_q	
+	TBCC	11	$\frac{1}{3}$	0.0545	0.0408	0.8047	0.4714	181	0.7072	$\kappa_P^{\text{WAVA}} \propto 2^{21.00}$	$\kappa_M^{\text{WAVA}} \propto 2^{21.58}$
■	TBCC	8	$\frac{1}{3}$	0.0365	0.0223	0.8906	0.5573	214	0.5981	$\kappa_P^{\text{WAVA}} \propto 2^{18.00}$	$\kappa_M^{\text{WAVA}} \propto 2^{18.58}$
◆	TBCC	11	$\frac{1}{4}$	0.0837	0.0709	0.6680	0.4180	214	0.5981	$\kappa_P^{\text{WAVA}} \propto 2^{21.00}$	$\kappa_M^{\text{WAVA}} \propto 2^{23.00}$
▲	TBCC	8	$\frac{1}{4}$	0.0640	0.0507	0.7441	0.4941	253	0.5059	$\kappa_P^{\text{WAVA}} \propto 2^{18.01}$	$\kappa_M^{\text{WAVA}} \propto 2^{20.00}$
*	PC	-	$\frac{1}{4}$	0.0778	0.0648	0.6875	0.4375	224	0.5714	$\propto 2^{15.17}$	$\propto 2^{15.17}$
●	PC	-	$\frac{1}{8}$	0.1819	0.1721	0.3584	0.2333	239	0.5358	$\propto 2^{16.32}$	$\propto 2^{16.32}$

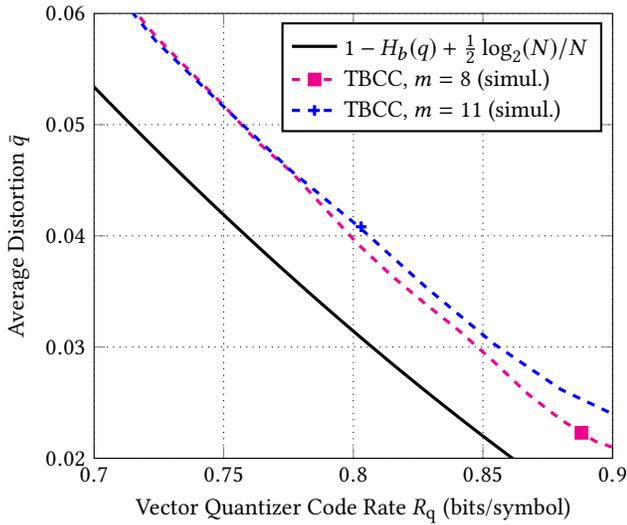


Figure 5: Code rate of the vector quantizer code C_q vs. average distortion \bar{q} for $N = 384$ bits and $P_B \leq 10^{-6}$.

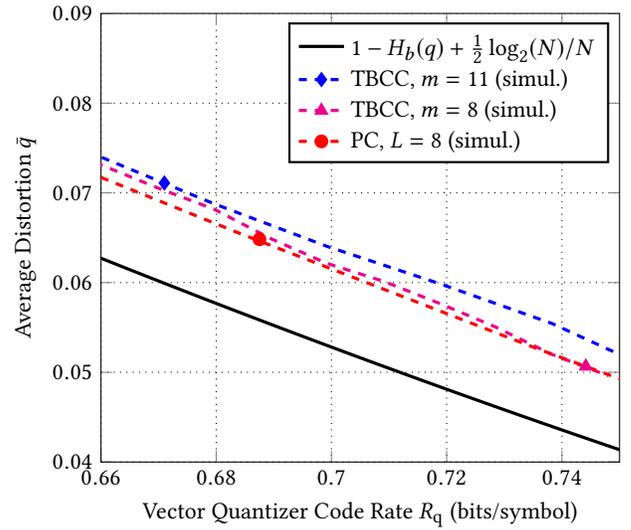


Figure 6: Code rate of the vector quantizer code C_q vs. average distortion \bar{q} for $N = 512$ bits and $P_B \leq 10^{-6}$.

7.2 Vector Quantization Performance

Using the approach described in Section 4 and setting $W_{\max} = 10^4$, we construct high rate codes to be used as a vector quantizer. Using Monte Carlo simulations, we plot the rate of these codes R_q vs. the measured average distortion \bar{q} in Figures 5 and 6 for $N = 384$,

corresponding to $R_s = \frac{1}{3}$, and for $N = 512$, corresponding to $R_s = \frac{1}{4}$, respectively.

We plot the approximate bound on the rate achieved for a given distortion from [23]. The approximated rate for block length N is

$$R_q^{(\text{approx})} \stackrel{\text{def}}{=} 1 - H_b(q) + \frac{\log_2(N)}{2N} + O\left(\frac{1}{N}\right) \quad (31)$$

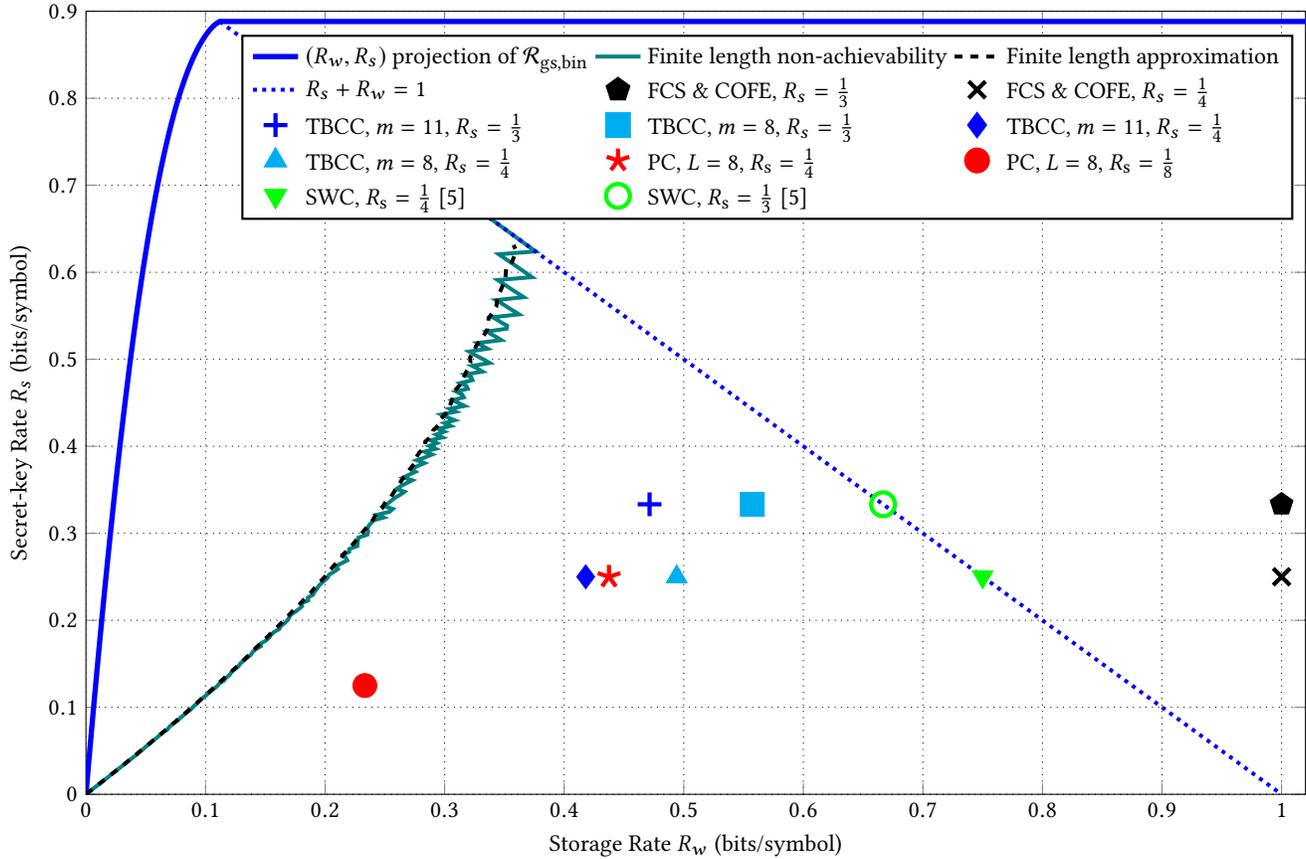


Figure 7: Storage-key rates for the GS model with $p_A = 0.0149$. The $(0.1118, 0.8882)$ bits/symbol point is the best possible point achieved by SW-coding (SWC) constructions such as polar codes (PCs) in [5], which lies on the dashed line representing $R_w + R_s = H(X)$. The PCs are designed by applying the design procedure proposed in [13] for WZ-coding with the SCL decoder with list size of L . The block-error probability satisfies $P_B \leq 10^{-6}$ and $K_s = 128$ bits for all codes. The finite length non-achievability bound and its approximation for $K_s = 128$ bits is depicted as well.

where $O(\cdot)$ denotes the big O notation. This approximation does not consider the effect of the constraint that the error correcting code designed in the previous subsection has to be a subcode of the vector quantizer of rate R_q . Therefore, this bound only gives an approximate achievable bound on the rate of the high-rate code that is used as a vector quantizer without having any constraint. The bound is plotted by neglecting the $O\left(\frac{1}{N}\right)$ term.

Using (23), we obtain the target distortion for the code to be designed, which allows to find a lower bound on the required rate R_q of the vector quantizer. The results are shown in Table 2. Observe that vector quantization performance of all codes is similar. Therefore, the code that has the best error correction performance yields the smallest rate for vector quantization, which corresponds to the smallest amount of helper data.

7.3 Overall Performance

Combining the results of the error correction and the vector quantizer performance, we can evaluate the key vs. storage rate ratio

by using (22). The intermediate and final results are listed in Table 2, and the achieved (R_w, R_s) tuples for all mentioned codes are depicted in Figure 7.

For the nested WZ-coding construction, where we have a vector quantizer and an error correcting code, we plot a finite length non-achievability (converse) bound. For a fixed key size of $K_s = 128$ bits and $p_A = 0.0149$, we evaluate the MC non-achievability bound for the error correcting code and combine this bound using (23) with the non-achievability bound from [23, (2.186)] for the vector quantizer code. A slightly tighter version of the non-achievability bound for the vector quantizer code can be found in [31]. To achieve a distortion of q , any vector quantizer code of blocklength N must satisfy [23, (2.186)]

$$\sum_{j=0}^{\lfloor Nq \rfloor} \binom{N}{j} \geq 2^{N(1-R_q)}. \quad (32)$$

Similar to the achievability bound discussed in Section 7.2, (31) is used to approximate also the non-achievability bound in (32). The combination of the MC bound and the converse bound for the vector

quantizer performance establishes a non-achievability bound on the best rate tuples that can be achieved for given parameters by our WZ-coding construction. In Figure 7, we plot this non-achievability bound using (32) and its approximation using (31). Note that the zigzag behaviour of the bound in (32) is due to the floor function. We observe a gap between these bounds and achieved rate tuples by the designed codes.

As discussed above, FCS and COFE have the key vs. storage ratio of

$$\frac{R_s}{R_w} = R_s \quad (33)$$

as the storage rate is 1 bit/symbol for these constructions. The SW coding constructions such as the syndrome coding method proposed in [5] achieve the ratio

$$\frac{R_s}{R_w} = \frac{R_s}{1 - R_s} \quad (34)$$

which improves on the FCS and COFE. WZ coding constructions with nested PC we constructed for $p_A = 0.0149$ based on the design procedure given in [13] achieves even larger ratios. The largest key vs. storage rate ratio is achieved by the TBCC with $R_s = \frac{1}{3}$ and $m = 11$ such that $\frac{R_s}{R_w} = 0.7072$. These results suggest that increasing the code rate R_s and the memory size of TBCCs allows a larger key vs. storage rate ratio.

8 CONCLUSION

We proposed a nested convolutional code construction, which might be useful for various achievability schemes. For the key agreement problem with PUFs, we proposed a design procedure for the nested code construction using TBCCs to obtain good reliability, secrecy, privacy, storage, and cost performance jointly. We implemented nested convolutional codes for practical source and channel parameters to illustrate the gains in terms of the key vs. storage rate ratio as compared to previous code designs. We observe that one variant of nested convolutional codes achieves a higher rate ratio than all other code designs in the literature but it may have a high hardware cost. Another variant of nested convolutional codes with low complexity is illustrated to perform similarly to the best previous codes in the literature. We also computed known finite-length bounds for our code construction to show the gaps between the performance of the designed codes and these bounds.

ACKNOWLEDGMENTS

This work was performed while O. Günlü was with the Chair of Communications Engineering, Technical University of Munich. O. Günlü was supported by the German Federal Ministry of Education and Research (BMBF) within the national initiative for “Post Shannon Communication (NewCom)” under the Grant 16KIS1004, and by the German Research Foundation (DFG) under grant KR 3517/9-1. V. Sidorenko is on leave from the Institute for Information Transmission Problems, Russian Academy of Sciences. His work was supported by the European Research Council (ERC) under the European Union’s Horizon 2020 research and innovation programme (grant agreement No 801434) and by the Chair of Communications Engineering at the Technical University of Munich.

The work of G. Kramer was supported by an Alexander von Humboldt Professorship endowed by the BMBF.

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