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Model Predictive Power Allocation for Hybrid Battery Balancing Systems

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Abstract—This work focuses on hybrid balancing systems, a recently-proposed concept that enables balancing of battery cells and hybridization with supercapacitors. To control this system, a model predictive control framework is developed. In addition to distributing the supercapacitor power among the balancing circuits, this framework is also able to minimize state-of-charge and thermal imbalances in the battery cells, as well as energy losses in the balancing circuits. The effectiveness of the proposed approach is verified via numerical simulations. It is shown that, in comparison with state-of-art balancing solutions, the proposed control approach is able to decrease battery stress in up to 9% and the maximum temperature in up to 4.5%.

I. INTRODUCTION

Batteries are one of the key elements for the successful deployment of electric vehicles (EVs). As discussed in several technological roadmaps [1], [2], EVs are pushing the performance goals of batteries, demanding higher durability, fast charging capability, reduced volume and mass, and lower cost. However, to achieve these goals, several challenges need to be addressed. One of the main challenges is the parameter variations present in battery packs, which appear due to unavoidable manufacturing tolerances and non-uniform aging of battery cells. These variations, together with large series connections of battery cells (typical in battery packs of EVs), introduce the so-called *weakest-cell problem*, i.e., cells with weaker thermal and capacity properties limit the performance of the entire battery pack [3]. Battery balancing systems, capable of equalizing voltage, state-of-charge and/or temperature of battery cells, are a promising solution to mitigate the *weakest-cell problem* [4], [5].

Another challenge in the design of battery packs lies in the selection of battery chemistries capable of simultaneously offering high energy density, high power density and long life. This difficulty has motivated the development of hybrid energy storage systems that combine batteries and supercapacitors [6]. The idea is to integrate energy storage units with complementary characteristics –batteries with high energy density and supercapacitors with high power density– such that the resulting hybrid energy storage system provides better overall performance [7], [8].

This work deals with hybrid balancing systems (HBSs), where battery balancing and hybridization functions share the same power converters. Fig. 1 shows one possible implementation of a HBS –reliant on a cell-to-cell shared configuration [9]– that allows the transfer of energy between: *i*)

different battery modules and *ii*) supercapacitors and battery modules. Thanks to this integrated power conversion, *energy management* and *power allocation & battery balancing* can be simultaneously performed. The *energy management* determines the power split between batteries and supercapacitors that minimize energy losses and battery stress. The *power allocation & battery balancing* allocates the supercapacitor power to the HBS balancing circuits and equalizes state-of-charge and temperature of the battery modules.

The main contribution of this work consists in the exploitation of control-allocation methods to tackle the *power allocation & battery balancing* problem of HBSs. In particular, we propose a model-predictive power allocation (MPPA) method to distribute the supercapacitor power between the balancing circuits of the HBS, while minimizing state-of-charge and thermal imbalances in the battery modules. This approach is particularly attractive, because of the MPPA’s capability to handle multi-variable control problems, like the HBS, and fulfillment of physical actuation constraints. To the best of the author’s knowledge, this represents one of the first contributions toward the decoupled control of HBSs.

II. PROBLEM FORMULATION

A. Overview of the HBS

Fig. 1 depicts the hybrid energy storage system, composed of batteries and supercapacitors, under consideration in this work. The battery pack consists of a series of n modules, each one containing one or more cells in parallel. The battery modules are connected with n bidirectional balancing circuits (based on dual half bridge converters) that allow the transfer of energy between supercapacitors and battery modules.

The control architecture of the balancing circuits is divided into three layers. The first layer, *energy management*, determines the amount of power (p_{SC}^*) that the supercapacitors should provide to the batteries taking into account energy efficiency and battery stress metrics. The second layer, *power allocation & battery balancing*, computes setpoints for the balancing currents $i_{B,j}^*$, $j = 1, \dots, n$, such that *i*) the supercapacitor power requested by the *energy management* (p_{SC}^*) is executed, and *ii*) thermal and state-of-charge (SoC) variations in the battery modules are minimized. The third layer consists of *inner current loops*¹ that control the balancing

¹in Fig. 1, the *inner current loops* are integrated into the balancing circuits and are not explicitly depicted

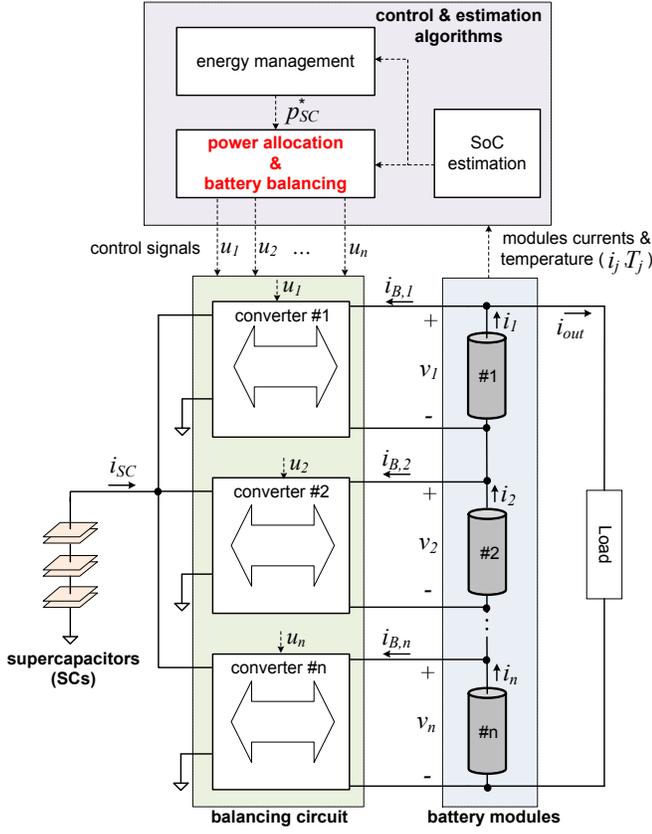


Fig. 1. Block diagram of the hybrid energy storage system (batteries and supercapacitors), and hybrid balancing system (balancing circuit and control). The control signals sent to the balancing circuit represent setpoints for the balancing currents ($u_j = i_{B,j}^*$).

currents $i_{B,j}$ of the HBS. In the sequel, we will focus on the design of the *power allocation & battery balancing* layer. The supercapacitor reference p_{SC}^* is assumed to be available, e.g. using state-of-the-art *energy management* methods [6]. Similarly, it is also assumed that balancing circuits are endowed with inner-current loops (see [10] for details).

B. Modeling

This section presents control-oriented models for the HBS. Accordingly, the electro-thermal model of the battery modules is described as [9]

$$\dot{q}_j = -\frac{1}{Q_j} i_j \quad (1)$$

$$v_j = f_j(q_j) - R_j i_j \quad (2)$$

$$C_{h,j} \dot{T}_j = R_j i_j^2 - \frac{1}{R_{cov,j}} (T_j - T_{env}) \quad (3)$$

for $j = 1, \dots, n$. The first equation represents the SOC (q_j) dynamics, where Q_j is the nominal capacity and i_j the current in the battery module j . The second equation approximates the terminal voltage (v_j) of the battery module j using the open-circuit voltage source $f_j(q_j)$ —dependent on the SoC—in series with an internal resistance (R_j). The third equation captures the thermal response of the battery module, where T_j is the

surface temperature, T_{env} the environmental temperature, $C_{h,j}$ the thermal capacitance and $R_{cov,j}$ the convective thermal resistance.

As shown in Fig. 1, the battery modules are connected in parallel with the vehicle load, i.e. the vehicle's powertrain. The power requested by this load is related with the battery voltage and load current (i_{out}) through the relation

$$p_{out} = \sum_j v_j i_{out} \quad (4)$$

while the load current is further linked with the battery and balancing currents through Kirchhoff's current law

$$i_j = i_{B,j} + i_{out} \quad (5)$$

The balancing circuits are modeled here as ideal dc-dc transformers. This means that the power extracted by the supercapacitors (p_{SC}) should be transferred to the battery modules and cover the power losses of the converters, which yields:

$$p_{SC} = -\sum_j v_j i_{B,j} + R_{B,j} i_{B,j}^2 \quad (6)$$

where $R_{B,j}$ captures the energy losses in the balancing circuit.

This work neglects the transient response of the *inner current loops*, i.e. $i_{B,j}^* \approx i_{B,j}$. This is justified by the large time-scale separation that exists between the fast response of the *inner current loops* (order of microseconds) and the slower response of the battery SoC and temperature (order of seconds and minutes).

To obtain a compact representation of the HBS, we stack the SoC, temperatures, and voltages of the battery modules into the vectors \mathbf{q} , \mathbf{v} , \mathbf{T} , and treat the balancing currents ($i_{B,j}$) as control variables (\mathbf{u}):

$$\mathbf{q} = [q_1 \dots q_n]^T, \quad \mathbf{v} = [v_1 \dots v_n]^T, \quad (7)$$

$$\mathbf{T} = [T_1 \dots T_n]^T, \quad \mathbf{u} = [i_{B,1} \dots i_{B,n}]^T \quad (8)$$

This leads to the following nonlinear state-space model

$$\dot{\mathbf{q}} = \mathbf{B}_q(\mathbf{u} + \mathbf{1}i_{out}) \quad (9a)$$

$$\mathbf{v} = \mathbf{f}(\mathbf{q}) - \mathbf{B}_v(\mathbf{u} + \mathbf{1}i_{out}) \quad (9b)$$

$$\dot{\mathbf{T}} = \mathbf{A}_T(\mathbf{T} - \mathbf{1}T_{env}) + \mathbf{B}_T \mathbf{g}(\mathbf{u} + \mathbf{1}i_{out}) \quad (9c)$$

$$p_{out} = \mathbf{v}^T \mathbf{1}i_{out} \quad (9d)$$

$$p_{SC} = -\mathbf{v}^T \mathbf{u} + \mathbf{r}_B^T \mathbf{g}(\mathbf{u}) \quad (9e)$$

where \mathbf{g} is a quadratic mapping, $\mathbf{1} \in \mathbb{R}^n$ a column vector filled with ones, \mathbf{f} a vector of open-circuit voltages and $\mathbf{B}_q, \mathbf{A}_T, \mathbf{B}_T, \mathbf{B}_v, \mathbf{r}_B$ matrices dependent on the battery and balancing circuit parameters. Table I presents the definition of all matrices and functions employed in this state-state model.

C. Control Requirements and Goals

The balancing currents are constrained by physical limits in the power converters:

$$\mathcal{U} = \{\mathbf{u} \in \mathbb{R}^n : \mathbf{u}_{min} \leq \mathbf{u} \leq \mathbf{u}_{max}\} \quad (10)$$

where $\mathbf{u}_{min}, \mathbf{u}_{max}$ represents the minimum and maximum balancing currents allowed by power converters in the HBS.

TABLE I
MATRICES OF THE HBS MODEL

Variable	Value
\mathbf{B}_q	$\text{diag}\left(-\frac{1}{Q_1}, \dots, -\frac{1}{Q_n}\right)$
\mathbf{A}_T	$\text{diag}\left(-\frac{1}{C_{h,1}R_{cov,1}}, \dots, -\frac{1}{C_{h,n}R_{cov,n}}\right)$
\mathbf{B}_T	$\text{diag}\left(-\frac{R_1}{C_{h,1}}, \dots, -\frac{R_n}{C_{h,n}}\right)$
\mathbf{B}_v	$\text{diag}(R_1, \dots, R_n)$
\mathbf{r}_B	$[R_{b,1} \dots R_{b,n}]^T$
$\mathbf{f}(\mathbf{q})$	$[f_1(q_1) \dots f_n(q_n)]^T$
$\mathbf{g}(\mathbf{u})$	$[u_1^2 \dots u_n^2]^T$
$\mathbf{1}$	$[1 \dots 1]^T$

To promote equalization of the battery modules, it is desirable to operate with small SoC variations [5]. These variations are usually defined as differences with respect to the average behavior of the battery pack, i.e., $\Delta q_i = q_i - \tilde{q}$, where $\tilde{q} = \frac{1}{n} \mathbf{1}^T \mathbf{q}$ is the average SoC in the battery pack. This is equivalent to:

$$\Delta \mathbf{q} = \left(\mathbf{I} - \frac{1}{n} \begin{bmatrix} \mathbf{1}^T \\ \vdots \\ \mathbf{1}^T \end{bmatrix} \right) \mathbf{q} = \mathbf{M} \mathbf{q} \quad (11)$$

where $\Delta \mathbf{q} = [\Delta q_1, \dots, \Delta q_n]^T$. Similarly, it is also helpful to minimize thermal imbalances in the battery pack. These imbalances are defined as $\Delta T_i = T_i - \tilde{T}$, where $\tilde{T} = \frac{1}{n} \mathbf{1}^T \mathbf{T}$ is the average temperature in the pack; this is equivalent to

$$\Delta \mathbf{T} = \mathbf{M} \mathbf{T} \quad (12)$$

where $\Delta \mathbf{T} = [\Delta T_1, \dots, \Delta T_n]^T$.

The main problem of this work can now be formulated as:

Problem 1. Consider the nonlinear state-space model (9) for the HBS. Find the balancing currents $\mathbf{u} \in \mathcal{U}$ that

(a) deliver the supercapacitor power requested by energy management layer:

$$p_{SC}^* = -\mathbf{v}^T \mathbf{u} + \mathbf{r}_B^T \mathbf{g}(\mathbf{u}) \quad (13)$$

(b) minimize SoC imbalances: $\|\Delta \mathbf{q}\| \rightarrow 0$

(c) minimize thermal imbalances: $\|\Delta \mathbf{T}\| \rightarrow 0$

(d) minimize energy losses in the balancing circuits: $\mathbf{u}^T \mathbf{R}_B \mathbf{u} \rightarrow 0$

where $\mathbf{R}_B = \text{diag}(\mathbf{r}_B)$.

III. POWER ALLOCATION AND BATTERY BALANCING

This section describes a model predictive approach to solve the *power allocation & battery balancing* problem introduced in the previous section.

A. Motivation

According to Problem 1, the allocation of the supercapacitor power is considered as the *primary control goal* and encoded as a hard constraint (13). Since this constraint is composed of one nonlinear equation with n unknown variables (i.e., the vector \mathbf{u}), multiple solutions might exist [11]. For example,

TABLE II
PARAMETERS OF THE HBS

Variable	Symbol	Value	Unit
Battery pack			
initial SoC	\mathbf{q}_0	[0.9, 0.92, 0.88]	V
nominal capacity	\bar{Q}_j	[4, 3, 3] × 3	A.h
internal resistance	R_{bat}	$[\frac{1}{4}, \frac{1}{3}, \frac{1}{3}] \times 51.16$	mΩ
open-circuit voltage	f_j	$3.207 + 0.33q_j$	V
stored energy		10×10.8	W.h
Supercapacitors			
stored energy		4×0.31	W.h
Thermal Parameters			
thermal capacitance	C_h	[161, 125, 125]	J/K
convective resistance	$R_{cov,j}$	[10.3, 13.7, 13.7]	K/W
environment temperature	T_{env}	25	°C
Balancing circuit			
inner resistance	$R_{B,j}$	[0.04, 0.04, 0.04]	Ω
Balancing parameters			
balancing current limits	\mathbf{u}_{max}	[5, 5, 5]	A
	\mathbf{u}_{min}	[-5, 5, 5]	A
controller weights	Δq_{max}	0.01	[-]
	ΔT_{max}	0.75	°C
	α_u	0.5	[-]
prediction horizon	N	10	[-]
sample time	τ_s	1	s

one can allocate the supercapacitor power to a single balancing circuit² or evenly split it among the balancing converters³. This multitude of solutions can be combined with optimization-based strategies to pursue *secondary control goals*, such as minimization of energy losses, SoC and thermal imbalances, as described in Problems 1b, 1c and 1d. In the literature, this type of problem is known as control allocation problem and is usually formulated as a constrained optimization problem. Since our *secondary control goals* depend on dynamic variables – such as SoC \mathbf{q} and temperature \mathbf{T} – we adopt here a model-predictive framework, inspired by [12], to solve Problem 1. We call this approach model-predictive power allocation (MPPA).

B. MPPA

In order to design the MPPA, let us first discretize the model (9) using Euler's method and sample time τ_s

$$\mathbf{q}[k+1] = \mathbf{q}[k] + \tau_s \mathbf{B}_q (\mathbf{u}[k] + \mathbf{1} i_{out}[k]) \quad (14a)$$

$$\mathbf{v}[k] = \mathbf{f}(\mathbf{q}[k]) - \mathbf{B}_v (\mathbf{u}[k] + \mathbf{1} i_{out}[k]) \quad (14b)$$

$$\mathbf{T}[k+1] = \mathbf{T}[k] + \tau_s \mathbf{A}_T (\mathbf{T}[k] - \mathbf{1} T_{env}[k]) + \tau_s \mathbf{B}_T \mathbf{g}(\mathbf{u}[k] + \mathbf{1} i_{out}[k]) \quad (14c)$$

$$p_{out}[k] = \mathbf{v}^T[k] \mathbf{1} i_{out}[k] \quad (14d)$$

$$p_{SC}^*[k] = -\mathbf{v}^T[k] \mathbf{u}[k] + \mathbf{r}_B^T \mathbf{g}(\mathbf{u}[k]) \quad (14e)$$

where k is the discrete time index.

The MPPA uses the above discrete model to predict the response of the HBS over a N -step horizon. It then maps the

² $p_{SC}^* = -v_l^T u_l + R_{B,l} u_l^2$ and $u_j = 0$ for $j \neq l$
³ $p_{SC}^*/n = -v_j^T u_j + R_{B,j} u_j^2$ for $j = 1, \dots, n$

primary and secondary control goals, defined in Problem 1, into the following optimization problem:

$$\min_{\mathbf{u}[k]} \sum_{k=1}^N \Delta \mathbf{q}[k]^T \mathbf{Q}_q \Delta \mathbf{q}[k] + \Delta \mathbf{T}[k]^T \mathbf{Q}_T \Delta \mathbf{T}[k] + \mathbf{u}[k]^T \mathbf{R} \mathbf{u}[k]$$

$$\text{s.t.} \quad (14), \quad \mathbf{u}[k] \in \mathcal{U}, \quad (15a)$$

$$\Delta \mathbf{q}[k] = \mathbf{M} \mathbf{q}[k], \quad \Delta \mathbf{T}[k] = \mathbf{M} \mathbf{T}[k] \quad (15b)$$

$$\mathbf{q}[0] = \mathbf{q}_0, \quad \mathbf{T}[0] = \mathbf{T}_0, \quad k = 0, \dots, N \quad (15c)$$

The first set of constraints deals with the discrete model and physical actuation limits, the second computes the SoC and thermal variations, and the third enforces initial conditions. Regarding the cost function: the first two terms penalize SoC and thermal imbalances over the prediction horizon, while the last term penalizes energy losses in the balancing circuit; the matrices $(\mathbf{Q}_q, \mathbf{Q}_T, \mathbf{R})$ are tuning weights, selected by the designer. To facilitate the tuning of these weights, the following normalization factors are introduced

$$\mathbf{Q}_q = \Delta q_{max}^{-2} \mathbf{I}, \quad \mathbf{Q}_T = \Delta T_{max}^{-2} \mathbf{I}, \quad \mathbf{R} = \alpha_u \mathbf{R}_B \quad (16)$$

where Δq_{max} represents the desired maximum SoC variation, ΔT_{max} the desired maximum temperature variation and \mathbf{I} the identity matrix. The parameter $\alpha_u \geq 0$ is a factor that allows the designer to explore trade-offs between energy losses and SoC/thermal imbalances. As a result of these normalization factors, the MPPA performance is ultimately dictated by the tuple $(\Delta q_{max}, \Delta T_{max}, \alpha_u)$.

We assume that the MPPA has access to the system states, either via measurement (\mathbf{T}) or estimation (\mathbf{q}) [13]. The variables $p_{SC}^*[k], p_{out}[k], T_{env}[k]$ are treated here as known exogenous inputs during the prediction horizon, which allows the incorporation of preview information in the optimization problem. The MPPA implementation follows a receding horizon strategy, i.e. at each time step the optimal solution of (15) is computed ($\mathbf{u}^*[k]$) and then the first element ($\mathbf{u}^*[0]$) applied to the HBS. Because of nonlinear constraints, e.g. (14e), finding unique optimal solutions for the MPPA is a challenge task. To assist in the search of optimal solutions, we employed the IPOPT [14], an interior-point nonlinear programming solver.

IV. VALIDATION RESULTS

A. Simulation Setup

This section presents validation results of the MPPA-based HBS using numerical simulations. The model of the HBS is based on a small scale prototype composed of 10 battery cells (cylindrical Li-ion LG 18650HG2), 4 SCs (Maxwell BCAP0310 P270 T10) and 3 balancing circuits. The battery cells were organized in a string of 3 modules: module #1 has 4 cells in parallel, while module #2 and #3 have 3 cells in parallel. This arrangement, which was also considered in our previous work [9], emulates a situation where the battery pack has significant imbalances in capacity and inner resistance. The parameter values of the battery modules are present in Table II. To transfer energy between the battery modules and SCs,

TABLE III
DEFINITION OF PERFORMANCE METRICS

1: average rms value of SoC differences	$\Delta q_{rms} = \frac{1}{n} (\Delta q_{1,rms} + \dots \Delta q_{n,rms})$
2: average rms value of temperature differences	$\Delta T_{rms} = \frac{1}{n} (\Delta T_{1,rms} + \dots \Delta T_{n,rms})$
3: maximum temperature in the battery modules	$T_{max} = \max_{j,k} T_j[k]$
4: minimum SoC at the end of the driving cycles	$q_{min} = \min_j q_j[M]$
5: average rms value of the battery current,	$i_{rms} = \frac{1}{n} (i_{1,rms} + \dots i_{n,rms})$
6: rms value of the current in the battery modules,	$i_{j,rms} = \sqrt{\frac{1}{M} \sum_{k=1}^M i_j^2[k] \tau_s}$

$M =$ duration of the driving cycle
rms = root-mean-square

TABLE IV
VALUE OF PERFORMANCE METRICS

	No Balancing (NB)	Active Balancing (AB)	Hybrid No Balancing (HNB)	Hybrid Balancing (HBS)
equalization				
$100 \times \Delta q_{rms}$	4.03%	0.45%	3.93%	0.48%
ΔT_{rms}	1.66°C	0.44°C	1.36°C	0.37°C
thermal/SOC				
T_{max}	38.10°C	36.10°C	35.80°C	34.20°C
$100 \times q_{min}$	27.66%	35.62%	29.74%	37.34%
battery stress				
i_{rms}	7.72 A	7.75 A	6.97 A	6.99 A
$i_{1,rms}$	7.72 A	8.87 A	6.97 A	8.00 A
$i_{2,rms}$	7.72 A	7.23 A	6.97 A	6.54 A
$i_{3,rms}$	7.72 A	7.13 A	6.97 A	6.42 A

Note: best values are highlighted in *blue*.

balancing circuits based on dual half bridge converters were considered. These converters employ phase-shift methods to control the balancing currents (see [10] for details).

The power requested to the energy storage system follows a scaled version of the US06 driving cycle, repeated 6 times and with a power scaling factor⁴ of 1 to 140. This driving cycle information is explored by the *energy management* layer to compute the supercapacitor power setpoint (p_{SC}^*) that minimizes energy losses in the hybrid energy storage system, using an optimization-based methodology similar to [9]. Besides the MPPA-based HBS, three additional variants were simulated:

- *no balancing* (NB) with $\mathbf{u} = 0$
- *active balancing* (AB), obtained by solving the MPPA problem (15) without the supercapacitor presence ($p_{SC}^* = 0$).
- *hybridization and no balancing* (HNB), obtained by solving the MPPA problem (15) parameterized with $\Delta q_{max} = \infty, \Delta T_{max} = \infty$

B. Performance Metrics

To quantify benefits of the HBS, several key performance metrics related with the equalization goals, thermal behavior, SoC discharge and battery stress were computed –see Table III

⁴reference vehicle for power calculation is the uCar [8]

for their definition. Inspecting the value of these metrics, represented in Table IV, reveals that the HBS provides: *i*) similar equalization performance as the AB; *ii*) similar average battery stress as the HNB and; *iii*) the best thermal behavior and SoC discharge metrics. In other words, the HBS combines the strengths of the active balancing and hybridization to produce the best overall performance. More specifically, the HBS, thanks to the power aid provided by the supercapacitors, is able to reduce the average root-mean-square current in the battery to 6.99Arms (a 9.4% decrease in comparison with NB and AB), which might lessen battery degradation. This stress reduction also decreases the maximum temperature of the battery pack to 34.2°C (i.e., −3.9°C than the NB configuration, −1.9°C than the AB and −1.6°C than HNB), which decreases the cooling needs of the battery. Finally, the HBS exhibits the highest SoC at the end of the driving cycle (9.6% higher than the NB, 1.7% higher than the AB and 7.6% higher than HNB), which contributes to an increase in the vehicle’s range.

V. CONCLUSIONS

This paper introduced a new control method for hybrid balancing systems. It exploited a decoupled control architecture, which divided the *energy management* and *power allocation & battery balancing* into two separate control layers. A systematic framework was then developed to model the hybrid balancing system and solve the *power allocation & battery balancing* with model-predictive and control allocation methods. Simulation results demonstrated that, in comparison with other state-of-art balancing systems, the proposed concept was able to simultaneously improve equalization, thermal, SoC and battery stress performance metrics. In future works, we plan to experimentally validate the proposed control method and evaluate its robustness against parameter uncertainty.

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