Successive Cancellation List Decoding of Product Codes with Reed-Muller Component Codes

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Abstract—This letter proposes successive cancellation list (SCL) decoding of product codes with Reed–Muller (RM) component codes. SCL decoding relies on a product code description based on the 2×2 Hadamard kernel, which enables interpreting the code as an RM subcode. The focus is on a class of product codes considered in wireless communication systems, based on single parity-check and extended Hamming component codes. For short product codes, it is shown that SCL decoding with a moderate list size performs as well as (and, sometimes, outperforms) belief propagation (BP) decoding. Furthermore, by concatenating a short product code with a high-rate outer code, SCL decoding outperforms BP decoding by up to 1.4 dB.

Index Terms—Product codes, Reed-Muller codes, polar codes, successive cancellation decoding, list decoding.

I. INTRODUCTION

PRODUCT codes [1] have gained considerable attention due to their suitability for low-complexity iterative decoding [2], [3]. Usually, product codes are constructed as two or three dimensional arrays, where each dimension is encoded by a short algebraic code. This choice allows the use of lowcomplexity soft-input soft-output (SISO) [3] or algebraic (e.g., bounded distance) [4]–[6] decoders for the component codes. Reed–Muller (RM) codes [7] and their majority logic decoding [8] were introduced roughly one year after product codes. Since then, RM codes have been analyzed intensively both from the code structure [9] and decoding [10], [11] points of view. Interest in RM codes has recently grown due to their close relationship with polar codes [12], [13]. It has been shown [9] that they achieve capacity under maximum a posteriori (MAP) decoding over binary erasure channel (BEC).

Product codes based on RM component codes have been considered, e.g., in [3], [14] for the case where the component codes are extended Hamming and single parity-check (SPC) codes. This choice of component codes has been considered in wireless communication systems (see, e.g., [15]–[17]) thanks to the availability of low-complexity SISO decoders for SPC and extended Hamming codes.¹

In this letter, product codes with SPC and/or extended Hamming component codes are considered. The emphasis is

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¹Extended Hamming codes can be efficiently decoded, for instance, by exploiting their compact trellis representation or by employing the suboptimum Chase-Pyndiah decoder proposed in [3].

on the short and moderate blocklength regimes due to the increasing interest in efficient error correcting codes for short packet transmissions required by emerging applications (see, e.g., [18]). Typically, encoding of product codes makes use of the component codes' systematic encoders, while decoding is performed iteratively. An equivalent code can be obtained by using non-systematic encoders for the component codes. In particular, an RM product code construction which directly maps the code structure onto the iterated Kronecker product of the 2×2 Hadamard kernel is considered [19]. Similar observations were used to increase the throughput of polar codes by reducing the decoding latency/complexity [20], [21]. In [20], the authors proposed a construction that allows interpreting a polar code as a 2-dimensional product code. The construction allows using successive cancellation (SC) decoders row- and column-wise to reduce the complexity/latency with respect to the case where SC decoding is performed over the larger polar code. Similarly, [21] proposed designing of product codes, where the component codes are polar codes. The focus was on reducing the latency by proposing a two-stage decoding, where the SC decoder of the large polar code is used only if the iterative product code decoder does not converge to a valid codeword. In this paper, we make use of this observation to employ standard polar code SC and successive cancellation list (SCL) decoding algorithms, instead of belief propagation (BP) decoding, for a class product codes of large practical interest.² Simulations on the binary input additive white Gaussian noise (B-AWGN) channel show that SCL decoding with small list sizes performs as good as BP decoding for the considered cases. The benefits of SCL decoding of product codes extends beyond the potential channel coding gains with respect to BP decoding. In fact, SCL decoding allows low-complexity decoding of the concatenation of the product code with a high-rate outer code, as proposed for polar codes in [23]. The concatenation provides remarkable gains over the product code alone, and over a BP decoder which jointly decodes the outer code and the inner product code. This behaviour is characterized by a weight enumerator analysis of the concatenated product codes, restricted to the minimum distance terms. Another important advantage of list decoding is to reduce the number of pilots for channel estimation when communicating over fading channels with unknown channel state [24].

The work is organized as follows. In Section II, we provide preliminaries needed for the rest of the work. In Section III, we

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²Recently, the SC decoding of product codes with SPC component codes has been introduced in [22] by using SPC kernels.

(n,k,d)	\mathcal{C}_1	\mathcal{C}_2	A_d
(128, 105, 4)	SPC (16, 15)	SPC (8,7)	3360
(128, 77, 8)	eH (16,11)	SPC (8,7)	3920
(256, 121, 16)	eH (16,11)	eH (16,11)	19600
(256, 225, 4)	SPC(16, 15)	SPC(16, 15)	14400
(1024, 693)	SPC (64, 63)	eH (16, 11)	282240

revisit the connection between RM codes and product codes with RM component codes. The concatenation with a highrate outer code is discussed in Section IV. Numerical results are provided in Section V. Conclusions follow in Section VI.

II. PRELIMINARIES

A. Product Codes

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A μ -dimensional (n, k, d) product code C [1] is obtained by *iterating* μ binary linear block codes $C_1, C_2, \ldots, C_{\mu}$. Let C_{ℓ} be the ℓ th component code with parameters $(n_{\ell}, k_{\ell}, d_{\ell})$, where n_{ℓ} , k_{ℓ} , and d_{ℓ} are its blocklength, dimension, and minimum Hamming distance, respectively and G_{ℓ} is the generator matrix. Then, the parameters of the resulting product code are the multiplication of the individual ones. Similarly, its generator matrix is given as

$$\boldsymbol{G} = \boldsymbol{G}_1 \otimes \boldsymbol{G}_2 \otimes \ldots \otimes \boldsymbol{G}_{\mu}.$$
 (1)

Although characterizating the distance spectrum of a product code is an elusive problem (with a few exceptions, see [25]), the minimum distance d and the multiplicity A_d of codewords with weight d can be obtained as $d = \prod_{\ell=1}^{\mu} d_{\ell}$ and $A_d = \prod_{\ell=1}^{\mu} A_{d_{\ell}}^{(\ell)}$, where $A_w^{(\ell)}$ is the multiplicity of the codewords having weight of w in the ℓ th component code. Examples of minimum distances and minimum weight multiplicities for some 2-dimensional product codes based on SPC and extended Hamming codes are provided in Table I. While achieving relatively large minimum distances, product codes based on these component codes suffer, in general, from large minimum weight multiplicity [14]. This observation is important to understand the gains attainable by concatenating product codes with high-rate outer codes.

B. Reed-Muller Codes

The construction of an r-th order RM code of length $n = 2^m$ and dimension $k = 1 + \binom{m}{1} + \binom{m}{2} + \cdots + \binom{m}{r}$, denoted by RM(r,m) with $0 \le r \le m$, starts by defining the $n \times n$ Hadamard matrix $G_n = K_2^{\otimes m}$ where $K_2^{\otimes m}$ denotes the *m*-fold Kronecker product of the Hadamard kernel

$$\boldsymbol{K}_2 \triangleq \begin{bmatrix} 1 & 0\\ 1 & 1 \end{bmatrix}$$

The RM code generator matrix G is obtained by removing the rows of G_n with weight lower than 2^{m-r} . We denote the set containing the indices of discarded rows (frozen bits) as A and let $v = (v_1, v_2, \ldots, v_k)$ contain the indices in the complement set A^C in an ascending order, i.e., $v_1 < v_2 < \ldots < v_k$. We

define the entry at the *i*th row and *j*th column of the $k \times n$ matrix F as

$$F_{i,j} = \begin{cases} 1 & \text{if } j = v_i \\ 0 & \text{otherwise.} \end{cases}$$

It follows that

$$\boldsymbol{G} = \boldsymbol{F}\boldsymbol{G}_n. \tag{2}$$

The *n*-dimensional frozen bit vector f, where $f_i = 0$ for all $i \in A$ and $f_i = 1$ otherwise, is also obtained by summing all the rows of F, i.e.,

$$\boldsymbol{f} = \boldsymbol{e}\boldsymbol{F} \tag{3}$$

where e is the length-k all-one vector. For the encoding, an *n*-bit vector $u = (u_1, u_2, ..., u_n)$ is defined, where $u_i = 0$ for all $i \in A$ and the remaining k elements of u consists of information bits. Encoding proceeds as $c = uG_n$.

III. REED-MULLER PRODUCT CODES

Consider a product code where the ℓ th component code C_{ℓ} is an $\text{RM}(r_{\ell}, m_{\ell})$ code with the corresponding row-selecting matrix F_{ℓ} (frozen bit vector f_{ℓ}). It is a subcode of the $\text{RM}(r_1 + r_2 + \ldots + r_{\mu}, m_1 + m_2 + \ldots + m_{\mu})$ code [19]. For the ℓ th component code, the generator matrix is obtained via (2) as

$$\boldsymbol{G}_{\ell} = \boldsymbol{F}_{\ell} \boldsymbol{K}_{2}^{\otimes m_{\ell}}.$$
(4)

Using the mixed-product property of the Kronecker product, the frozen bit vector for the resulting product code is obtained.

Proposition 1. The generator matrix of the product code obtained by iterating μ RM codes $\text{RM}(r_1, m_1)$, $\text{RM}(r_2, m_2)$, ..., $\text{RM}(r_{\mu}, m_{\mu})$ is given by

$$\boldsymbol{G} = \boldsymbol{F} \boldsymbol{K}_2^{\otimes (m_1 + m_2 + \ldots + m_\mu)}$$
(5)

where $F = F_1 \otimes F_2 \otimes \ldots \otimes F_{\mu}$, resulting in

$$\boldsymbol{f} = \boldsymbol{f}_1 \otimes \boldsymbol{f}_2 \otimes \ldots \otimes \boldsymbol{f}_{\mu}. \tag{6}$$

Proof. Follows from the application of the mixed-product property to the combination of (1), (3) and (4):

$$(F_1 K_2^{\otimes m_1}) \otimes (F_2 K_2^{\otimes m_2}) \otimes \cdots \otimes (F_\mu K_2^{\otimes m_\mu}) = (F_1 \otimes F_2 \otimes \cdots \otimes F_\mu) (K_2^{\otimes m_1} \otimes K_2^{\otimes m_2} \otimes \cdots \otimes K_2^{\otimes m_\mu}). \Box$$

Example 1. Consider a two-dimensional product code with a (2, 1) repetition code and a (4, 3) SPC code as component codes with $F_1 = \begin{bmatrix} 0 & 1 \end{bmatrix}$ yielding $f_1 = \begin{bmatrix} 0 & 1 \end{bmatrix}$ and

$$\boldsymbol{F}_2 = \begin{bmatrix} 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix}$$

yielding $f_2 = \begin{bmatrix} 0 & 1 & 1 & 1 \end{bmatrix}$. Then, the product code generator matrix is obtained via (5) where

$$\boldsymbol{F} = \begin{bmatrix} 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

with $f = \begin{bmatrix} 0 & 0 & 0 & 0 & 0 & 1 & 1 & 1 \end{bmatrix}$ via (6).

Proposition 1 provides an interpretation of product codes with RM component codes as RM subcodes, where the frozen This article has been accepted for publication in a future issue of this journal, but has not been fully edited. Content may change prior to final publication. Citation information: DOI 10.1109/LCOMM.2019.2937773, IEEE Communications Letters

bit positions are given as (6). This enables the use of the SC and SCL decoding algorithms derived for RM and polar codes [12], [13], [23] to decode this class of product codes.

Remark 1. Equivalent codes, defined by different frozen bit vectors, can be obtained as the Kronecker product is not commutative. The definition of the different frozen bits vectors is related to the order with which the component codes generator matrices are iterated in (1).

A. Successive Cancellation (List) Decoding

As for polar codes [12], SC decoding for the construction illustrated above estimates the information bits bit u_i , i = 1, 2, ..., n, by using the channel observation $y = (y_1, y_2, ..., y_n)$ and the previous decisions $\hat{u}_1, \hat{u}_2, ..., \hat{u}_{i-1}$, taking into account the code constraints imposed by the set \mathcal{A} . The decoding equations are defined by K_2 and used recursively to compute the soft-information for the bits. In SCL decoding [23], two hypothesis are kept open for each bit u_i if it is not a frozen bit. Whenever the number of the hypothesis exceeds a given maximum list size L, they are pruned by keeping the most likely ones according to the computed metrics. At the final stage, the decoder outputs the most likely candidate as the message estimate.

Remark 2. For polar codes, it was shown in [23] that a performance close to the one of an maximum likelihood (ML) decoder can be attained with a sufficiently large list size. This was demonstrated by computing a numerical lower bound on the ML decoding error probability via Monte Carlo simulation, where the correct codeword is introduced artificially in the final list, prior to the final selection. If for a specific list size L the simulated error probability is close to the numerical ML decoding lower bound, then increasing the list size L would not yield visible improvement. We shall see in Section V that the same principle applies to SCL decoding of product codes.

IV. CONCATENATION WITH A HIGH-RATE OUTER CODE

Following [23], we analyze the performance under SCL decoding of product codes based on SPC and extended Hamming component codes, in concatenation with a high-rate outer code. The outer code is used to test each codeword in the final list when SCL decoding is used. Among the survivors, the most likely one is chosen as the final decision. A reason to analyze such concatenation lies (besides in the obviously expected performance improvement) in the fact that actual schemes employing product codes may make use of an error detection code to protect the product code information message. We may hence consider sacrificing (part of) the error detection capability for a larger coding gain. Following the construction adopted in the IEEE 802.16 standard [17], we consider product codes with systematic encoding.

For product codes, large gains are expected by adding a high-rate outer code, especially at moderate-low error rates. This follows from the fact that product codes with RM component codes are characterized by a fairly large multiplicity of minimum weight codewords, as already observed in Section II-A. When using a product code based on RM component codes to transmit over a memory-less binary-input outputsymmetric channel with Bhattacharyya parameter β , the block error probability under BP decoding can be well approximated by the ML decoding truncated union bound (TUB)

$$P_B \simeq A_d \beta^d \tag{7}$$

already at moderate error rates [14]. Recalling Remark 2, the error probability under SCL decoding is thus limited by the ML decoding performance already at moderate error rates. By a suitable choice of the outer code, the multiplicity of minimum weight codewords may be considerably lowered, hence, reducing its contribution to the overall block error probability under ML decoding. This may potentially yield remarkable gains also under sub-optimum BP/SCL decoding. We analyze the impact of the outer code in a concatenated ensemble setting from a weight distribution perspective by focusing on the minimum weight terms only.

A. Average Weight Distribution of Concatenated Ensembles

We consider the concatenation of an (n_i, k_i) inner product code C_i with an (n_o, k_o) high-rate outer code C_o . Note that $k_i = n_o$. We denote by d the minimum distance of the inner product code. We further define the generator matrices of C_i and C_o as G_i and G_o , respectively.

Definition 1 (Concatenated Ensemble). The (serially) concateneted ensemble $\mathscr{C}(\mathcal{C}_{o}, \mathcal{C}_{i})$ is the set of all codes with generator matrix of the form $\mathbf{G} = \mathbf{G}_{o} \Pi \mathbf{G}_{i}$, where Π is an $n_{o} \times n_{o}$ permutation matrix.

Denote the outer code weight enumerator by A_j° . The minimum-weight input-output weight enumerator of the inner product code is given by $A_{j,d}^{i}$. The expected number of weightd codewords for a code drawn randomly from $\mathscr{C}(\mathcal{C}_{\circ}, \mathcal{C}_{i})$ is

$$\bar{A}_d = \sum_{j=1}^{n_{\rm o}} \frac{A_j^{\rm o} A_{j,d}^{\rm i}}{\binom{n_{\rm o}}{j}}$$

The expected multiplicity of weight-*d* codewords A_d can be used in (7) to obtain an estimate of the ensemble average error probability in the low error probability regime. If G_i is in systematic form, $A_{j,d}^i$ is easily computed from the input-output weight enumerators of the component codes [26, Thm. 1].

Example 2. Consider the (128,77) systematic product code with (16,11) extended Hamming and (8,7) SPC component codes, which has minimum distance 8 with a multiplicity of 3920. The code is concatenated with an outer cyclic redundancy check (CRC)-7 code with generator polynomial $g(x) = x^7 + x^3 + 1$. The resulting code is a member of a concatenated ensemble with an expected number of weight-8 codewords given by $\overline{A}_8 \simeq 26.4$, i.e., the multiplicity of weight-8 codewords is reduced, on average, by two orders of magnitude. The contribution of these codewords to the ensemble average error probability is reduced significantly. Hence, the TUB shall be approached only at low error rates.

Note that the generator matrix of the product code constructed according to Proposition 1 is not in systematic form. 4

Assume the inner code generator matrix G_i to be systematic. The overall code generator matrix can be written as

$$G = G_{ ext{o}} \Pi G_{ ext{i}} = G_{ ext{o}} \Pi S G_{ ext{i}, ext{nsys}} = G_{ ext{mo}} G_{ ext{i}, ext{nsys}}$$

where Π is the interleaver matrix, S is a $k_i \times k_i$ nonsingular matrix and $G_{i,nsys} = S^{-1}G_i$ is the non-systematic generator matrix according to Proposition 1. Furthermore, G_{mo} is defined to be the product $G_0\Pi S$. Thus, the SCL decoding can be used for the inner product code, where the modified outer code with generator matrix G_{mo} is used to test the codewords of the final list prior to a decision.

V. NUMERICAL RESULTS

We provide simulation results for two product codes, over the B-AWGN channel. The results are provided in terms of codeword error rate (CER) vs. signal-to-noise ratio (SNR), where the SNR is expressed as E_b/N_0 ratio (E_b is here the energy per information bit, and N_0 the single-sided noise power spectral density.) For both codes, the SCL decoding performance is compared to Gallager's random coding bound (RCB) [27] and the random coding union bound (RCUB) from [28, Thm. 16]. As a reference, the performance under BP is provided with a maximum number of iterations set to 100. The component codes are decoded by MAP SISO decoding over the component code trellis. For both product codes, the concatenation with a high-rate outer code is also considered. For the short construction, BP decoding of the concatenated scheme is also provided, where the product code Tanner graph is modified by adding a check node representing the outer code constraints (as for the component codes, the outer code is decoded within the node by a MAP SISO decoder). The TUB in the tighter form of [14, Eq. 3] is also provided.

The first product code is the (128, 77) code from Example 1, whose performance is depicted in Figure 1a. In particular, the component codes C_1 and C_2 are (16, 11) extended Hamming and (8,7) SPC codes, respectively. List decoding with L = 4is sufficient to approach the performance of BP decoding. With L = 8, the SCL decoder tightly matches the ML lower bound below CER of 10^{-2} . The gap to the RCUB is limited to 2 dB at CER of 10^{-6} . In the same Figure, the performance of a (128, 77) polar code under SCL decoding is provided. While the ML decoding performance of the polar code and of the product code are very close, the polar code requires a smaller list size to approach the ML lower bound. Figure 1b shows the performance by concatenating the (128, 77) product code with an outer CRC code with generator polynomial g(x) = $x^7 + x^3 + 1$, leading to a (128, 70) code. The performance of the concatenated scheme is provided for two interleavers between the inner and outer code. The label "no interleaver" denotes the trivial interleaver, i.e., Π is chosen to be the $k_i \times k_i$ identity matrix, while in the second case a random interleaver is used. The concatenation without interleaving performs remarkably well under SCL decoding. At a CER of 10^{-6} , SCL decoding of the concatenated code achieves gains up to 1.4 dB over the original product code. The gains attained by SCL decoding over BP decoding range from 1 dB at a CER of 10^{-2} to 1.4 dB at a CER $\approx 10^{-5}.$ The gap to the RCUB is 0.5 dB



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Fig. 1. CER vs. SNR under SCL decoding with various list sizes for the (a) (128, 77) product code and (b) (128, 70) concatenated ones, compared with a BP decoding.

at a CER $\approx 10^{-7}$. In this specific case, the omission of an interleaving stage yields a code performing better than the ensemble average. For sake of completeness, the performance of a concatenation employing a randomly generated interelaver is provided. The result tightly approaches, in this case, the expected ensemble performance approximated by the TUB.

A longer (1024, 693) product code has been constructed by choosing C_1 and C_2 to be the (16, 11) extended Hamming and the (64, 63) SPC codes, respectively. The results of the product codes with³ and without outer codes are provided in Figure 2. The outer CRC code has a generator polynomial $g(x) = x^{10} + x^9 + x^5 + x^4 + x + 1$, leading to a (1024, 683) code. The concatenated schemes needs a larger list than the one required by the product code alone to approach the ML lower bound, especially at high error rates. In particular, for long blocklengths the required list size increases due to the suboptimal choice of a large number of non-frozen bits enforced by the specific product code construction.

³For this case, the performance under BP is not provided. The reason is that the addition of the outer code check node in the product code Tanner graph resulted in large performance degradation, due to the emergence of a number of small trapping sets for the BP decoder.



Fig. 2. CER vs. SNR under SCL decoding with various list sizes for (a) (1024, 693) product code and (b) (1024, 683) concatenated one.

VI. CONCLUSIONS

Successive cancellation list (SCL) decoding of product codes with single parity-check and extended Hamming component codes has been investigated. SCL decoding relies on a product code description based on the 2×2 Hadamard kernel, which enables interpreting the code as a Reed-Muller subcode. With small list sizes, SCL decoding performs as good as (and sometimes it outperforms) belief propagation decoding. Larger gains are attained by concatenating an inner product code with an outer high-rate code. For specific concatenated constructions, a performance within a few tenths of a decibel from finite length bounds at CER $\approx 10^{-7}$ is achieved.

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REFERENCES

P. Elias, "Error-free coding," *IRE Trans. Inf. Theory*, vol. PGIT-4, pp. 29–37, Sep. 1954.

- [2] R. Tanner, "A recursive approach to low complexity codes," *IEEE Trans. Inf. Theory*, vol. 27, no. 5, pp. 533–547, Sep. 1981.
- [3] R. M. Pyndiah, "Near-optimum decoding of product codes: block turbo codes," *IEEE Trans. Commun.*, vol. 46, no. 8, pp. 1003–1010, Aug. 1998.
- [4] N. Abramson, "Cascade decoding of cyclic product codes," *IEEE Trans. Commun. Tech.*, vol. 16, no. 3, pp. 398–402, Jun. 1968.
- [5] X. Tang and R. Koetter, "Performance of iterative algebraic decoding of codes defined on graphs: An initial investigation," in *IEEE Inf. Theory Workshop*, Sep. 2007, pp. 254–259.
- [6] C. Häger and H. D. Pfister, "Approaching miscorrection-free performance of product codes with anchor decoding," *IEEE Trans. Commun.*, vol. 66, no. 7, pp. 2797–2808, Jul. 2018.
- [7] D. E. Muller, "Application of boolean algebra to switching circuit design and to error detection," *Trans. of the IRE Professional Group* on Electronic Computers, vol. EC-3, no. 3, pp. 6–12, Sep. 1954.
- [8] I. Reed, "A class of multiple-error-correcting codes and the decoding scheme," *Trans. of the IRE Professional Group on Inf. Theory*, vol. 4, no. 4, pp. 38–49, Sep. 1954.
- [9] S. Kudekar, S. Kumar, M. Mondelli, H. D. Pfister, E. Şaşoğlu, and R. L. Urbanke, "Reed-Muller codes achieve capacity on erasure channels," *IEEE Trans. Inf. Theory*, vol. 63, no. 7, pp. 4298–4316, Jul. 2017.
- [10] I. Dumer, "Recursive decoding and its performance for low-rate Reed-Muller codes," *IEEE Trans. Inf. Theory*, vol. 50, no. 5, pp. 811–823, May 2004.
- [11] I. Dumer and K. Shabunov, "Soft-decision decoding of Reed-Muller codes: recursive lists," *IEEE Trans. Inf. Theory*, vol. 52, no. 3, pp. 1260– 1266, Mar. 2006.
- [12] E. Arıkan, "Channel polarization: A method for constructing capacityachieving codes for symmetric binary-input memoryless channels," *IEEE Trans. Inf. Theory*, vol. 55, no. 7, pp. 3051–3073, Jul. 2009.
- [13] N. Stolte, "Rekursive Codes mit der Plotkin-Konstruktion und ihre Decodierung," Ph.D. dissertation, TU Darmstadt, 2002.
 [14] F. Chiaraluce and R. Garello, "Extended Hamming product codes
- [14] F. Chiaraluce and R. Garello, "Extended Hamming product codes analytical performance evaluation for low error rate applications," *IEEE Trans. Wireless Commun.*, vol. 3, no. 6, pp. 2353–2361, Nov. 2004.
- [15] C. Berrou, R. Pyndiah, P. Adde, C. Douillard, and R. Le Bidan, "An overview of turbo codes and their applications," in *Proc. The European Conference on Wireless Technology*, 2005, pp. 1–9.
- [16] M. C. Valenti, "Channel coding for IEEE 802.16 e mobile WiMAX," in *Tutorial, IEEE Int. Conf. Commun. (ICC)*, 2009.
- [17] "IEEE standard for air interface for broadband wireless access systems," IEEE Std 802.16-2017 (Revision of IEEE Std 802.16-2012), pp. 1–2726, Mar. 2018.
- [18] M. C. Coşkun, G. Durisi, T. Jerkovits, G. Liva, W. Ryan, B. Stein, and F. Steiner, "Efficient Error-Correcting Codes in the Short Blocklength Regime," *Elsevier Physical Commun.*, vol. 34, pp. 66–79, Jun. 2019.
- [19] A. J. Salomon and O. Amrani, "Augmented product codes and lattices: Reed-Muller codes and Barnes-Wall lattices," *IEEE Trans. Inf. Theory*, vol. 51, no. 11, pp. 3918–3930, Nov. 2005.
- [20] A. Pamuk and E. Arkan, "A two phase successive cancellation decoder architecture for polar codes," in *Proc. of IEEE Int. Symp. on Inf. Theory*, Jul. 2013, pp. 957–961.
- [21] V. Bioglio, C. Condo, and I. Land, "Construction and decoding of product codes with non-systematic polar codes," *CoRR*, vol. abs/1901.06892, 2019. [Online]. Available: http://arxiv.org/abs/1901.06892
- [22] M. C. Coşkun, G. Liva, A. Graell i Amat, and M. Lentmaier, "Successive cancellation decoding of single parity-check product codes," in *Proc. of IEEE Int. Symp. on Inf. Theory*, Jun. 2017, pp. 1758–1762.
- [23] I. Tal and A. Vardy, "List decoding of polar codes," *IEEE Trans. Inf. Theory*, vol. 61, no. 5, pp. 2213–2226, May 2015.
- [24] M. C. Coşkun, G. Liva, J. Östman, and G. Durisi, "Low-complexity joint channel estimation and list decoding of short codes," in *Proc. ITG Int. Conf. Syst., Commun. and Coding*, Feb. 2019.
- [25] G. Caire, G. Taricco, and G. Battail, "Weight distribution and performance of the iterated product of single-parity-check codes," in *IEEE GLOBECOM*, Nov. 1994, pp. 206–211.
- [26] M. El-Khamy, "The average weight enumerator and the maximum likelihood performance of product codes," in *Int. Conf. on Wireless Networks, Commun. and Mobile Computing*, June 2005, pp. 1587–1592.
- [27] R. Gallager, "A simple derivation of the coding theorem and some applications," *IEEE Trans. Inf. Theory*, vol. 11, no. 1, pp. 3–18, Jan. 1965.
- [28] Y. Polyanskiy, V. Poor, and S. Verdù, "Channel coding rate in the finite blocklength regime," *IEEE Trans. Inf. Theory*, vol. 56, no. 5, pp. 2307– 235, May 2010.